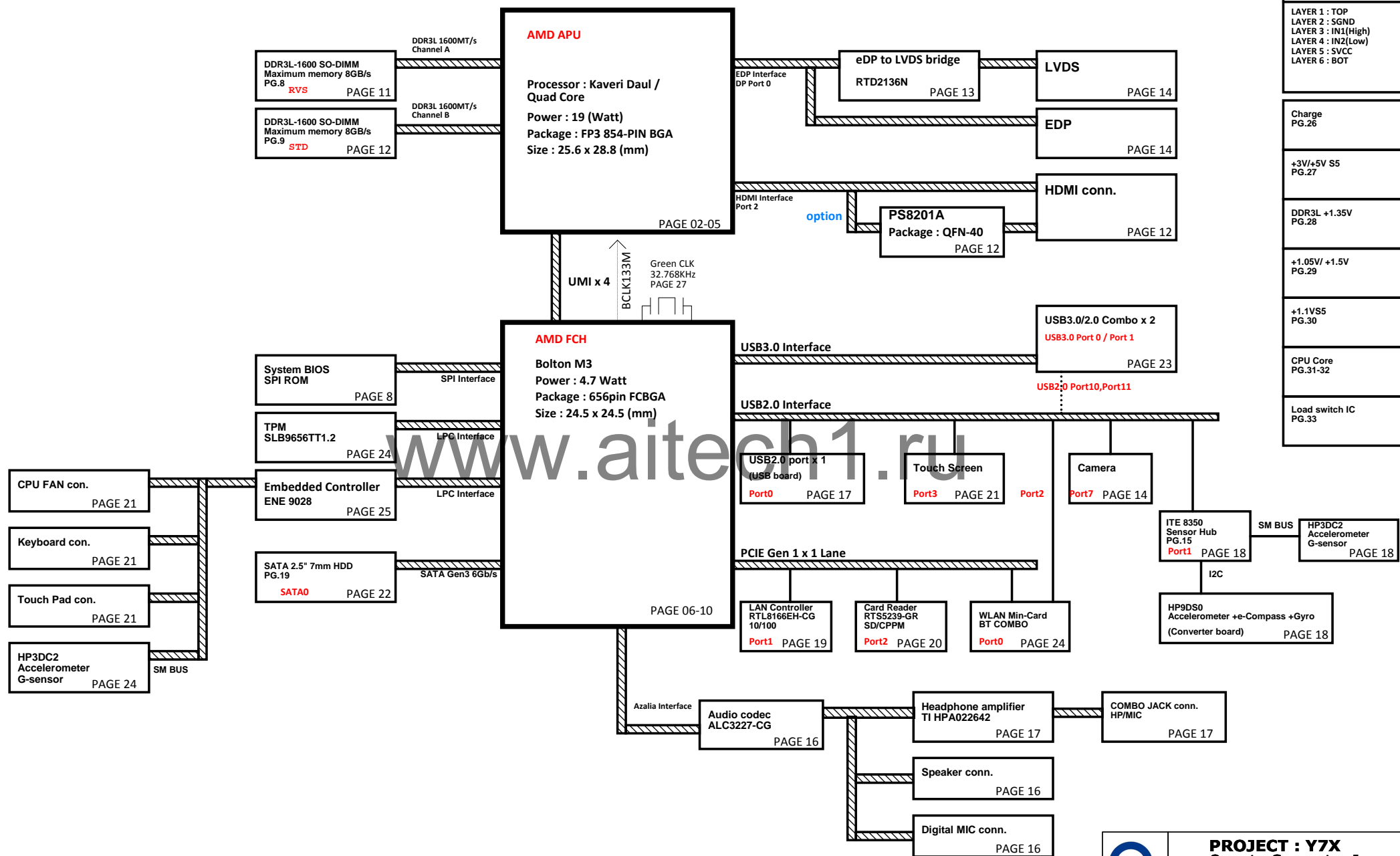
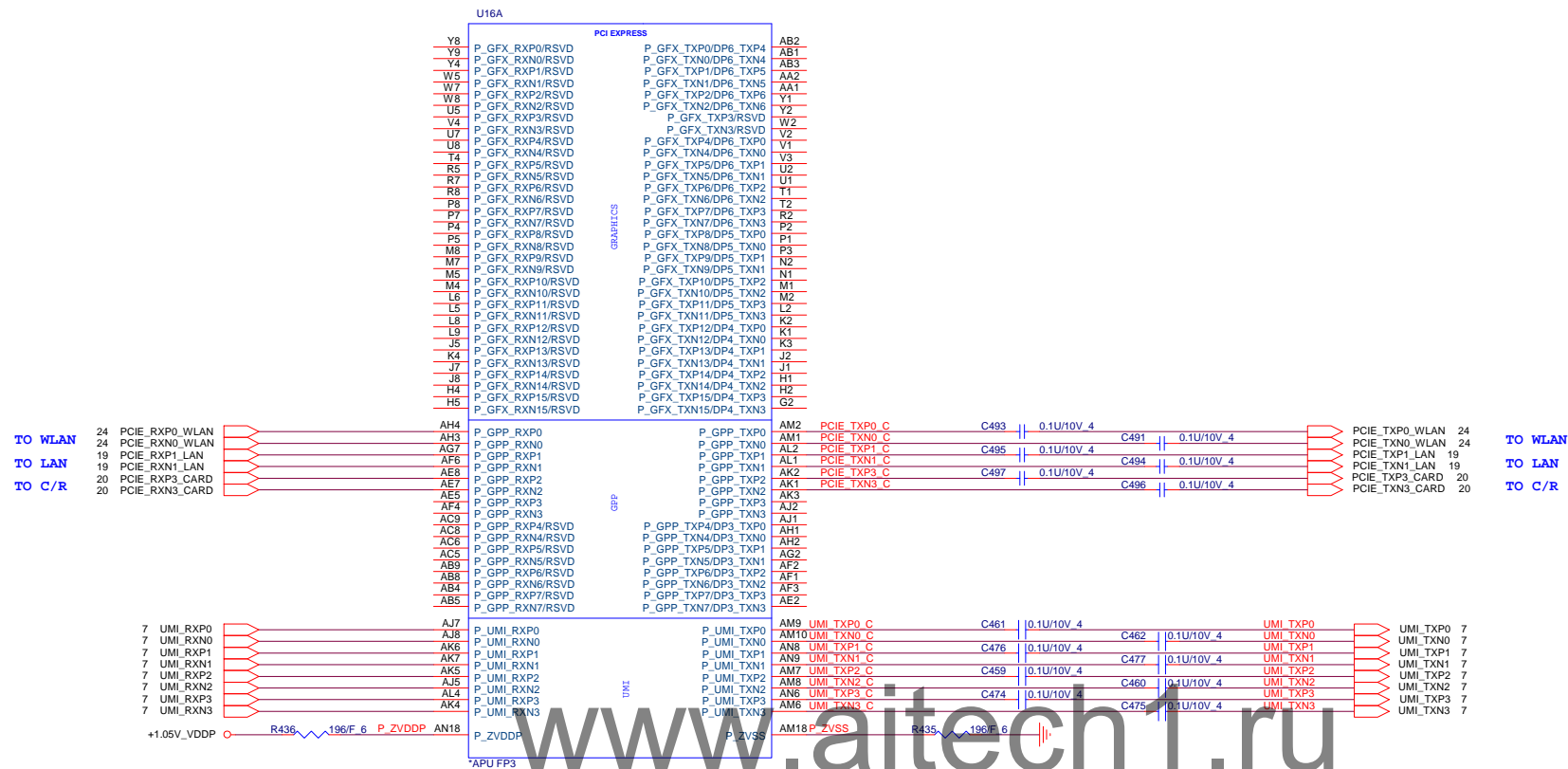
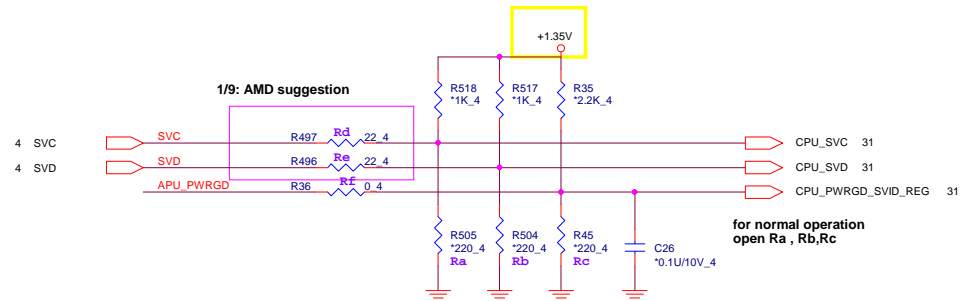
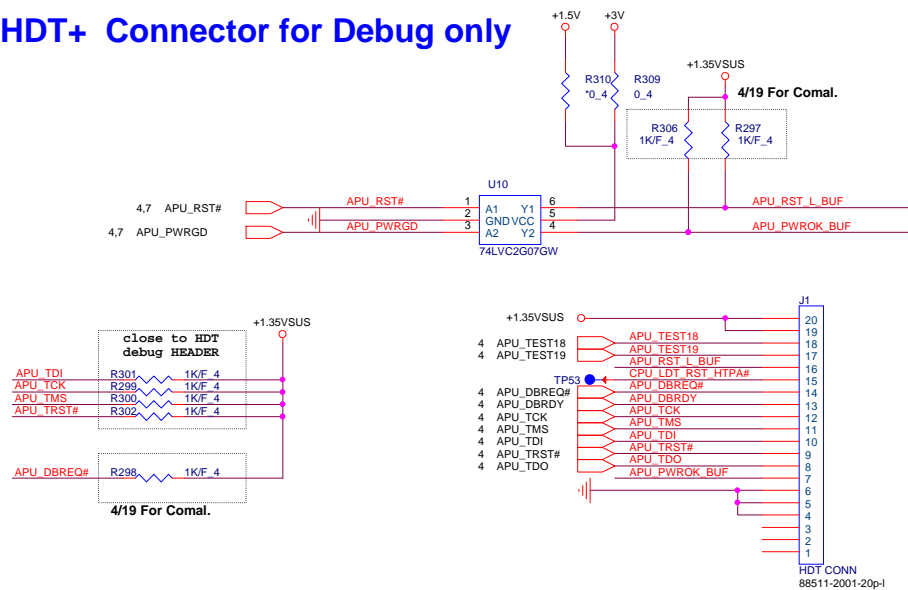


Y7X (HP Napa) AMD KAVERI Platform Block Diagram



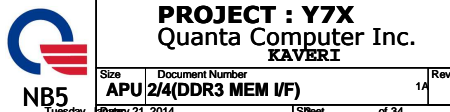


HDT+ Connector for Debug only

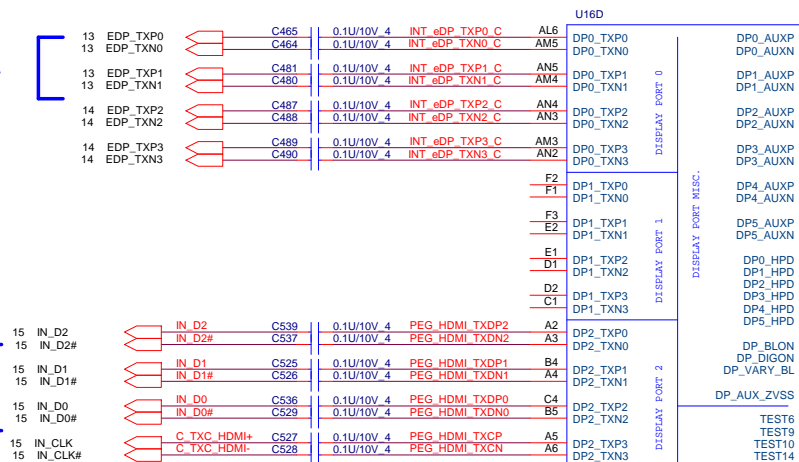


BOOT VOLTAGE			
SVC	SVD	VFIX_+VDD =VCC/GND	VFIX_+VDD =OPEN
0	0	1.1	1.1
0	1	1.0	1.2
1	0	0.9	1.0
1	1	0.8	0.8

4,6,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,24,25,33 +1.35V
4,15,16,24,29 +1.5V
3,4,5,11,12,22,28,33 +1.35VSUS
4,5 +1.05V_VDDP



DP0 output to
eDP to LVDS converter



4/19 HDMI change to DP2 for Comal.

DP2 output to
HDMI connector

note --HDMI P&N can not swap

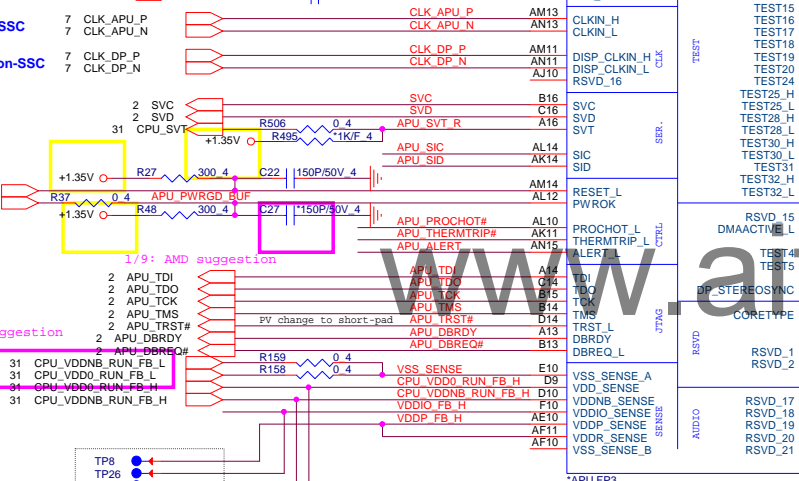
Note: CLK APU HCLKP/N is 100MHZ SSC

Note: CLK_DP_NSSCP/N is 100MHZ non-SSC

APU Core Power

FCH to APU

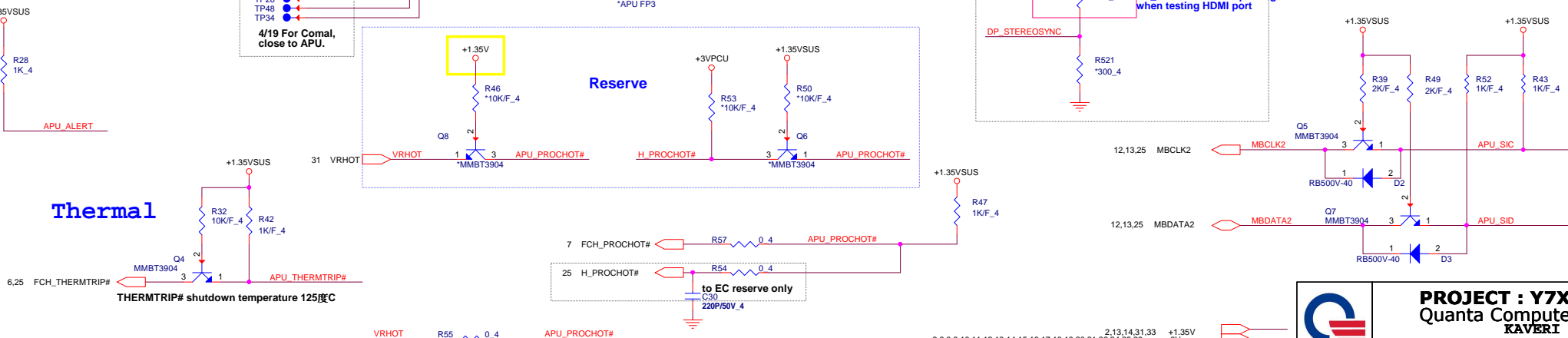
EC to APU

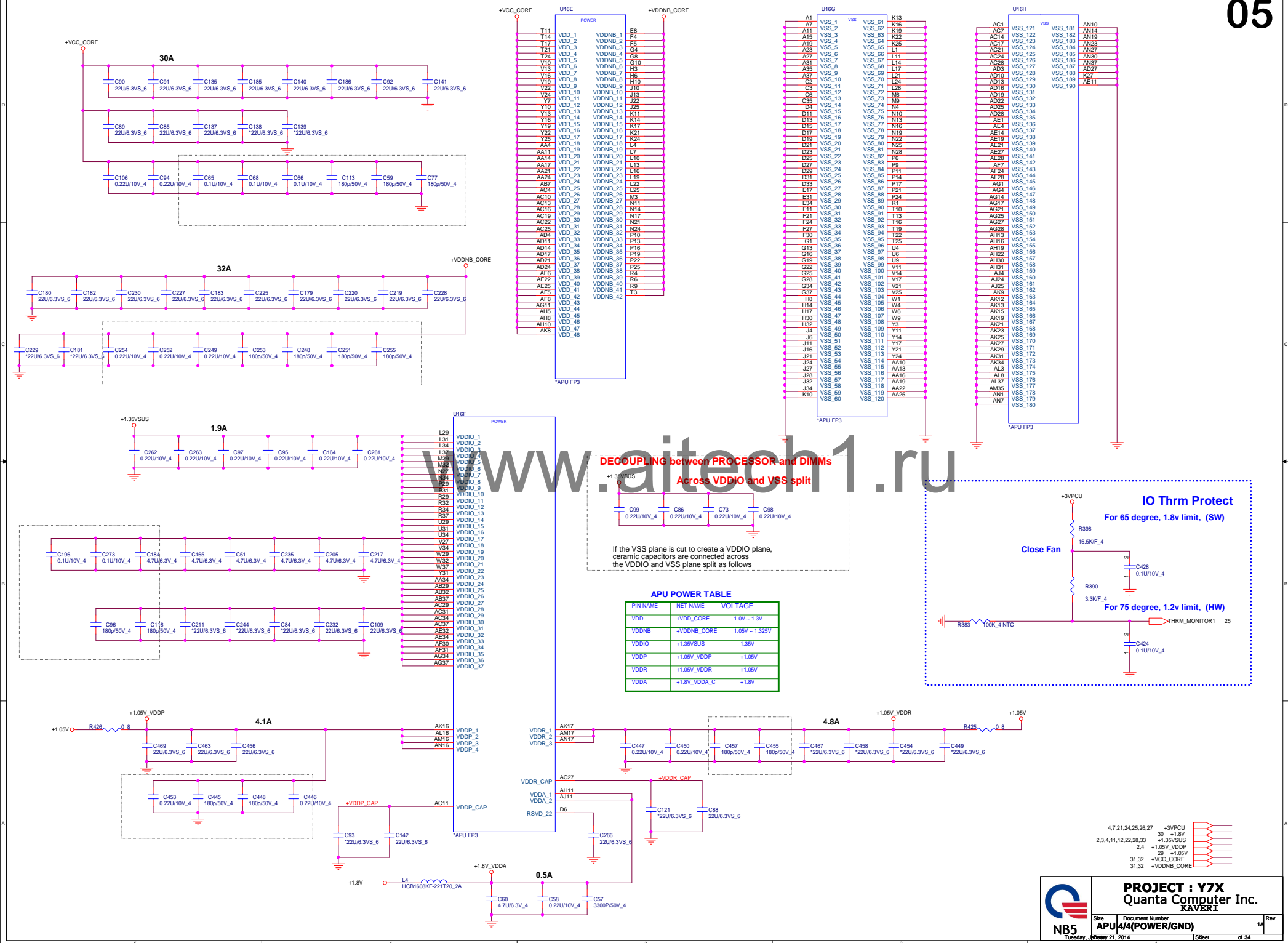


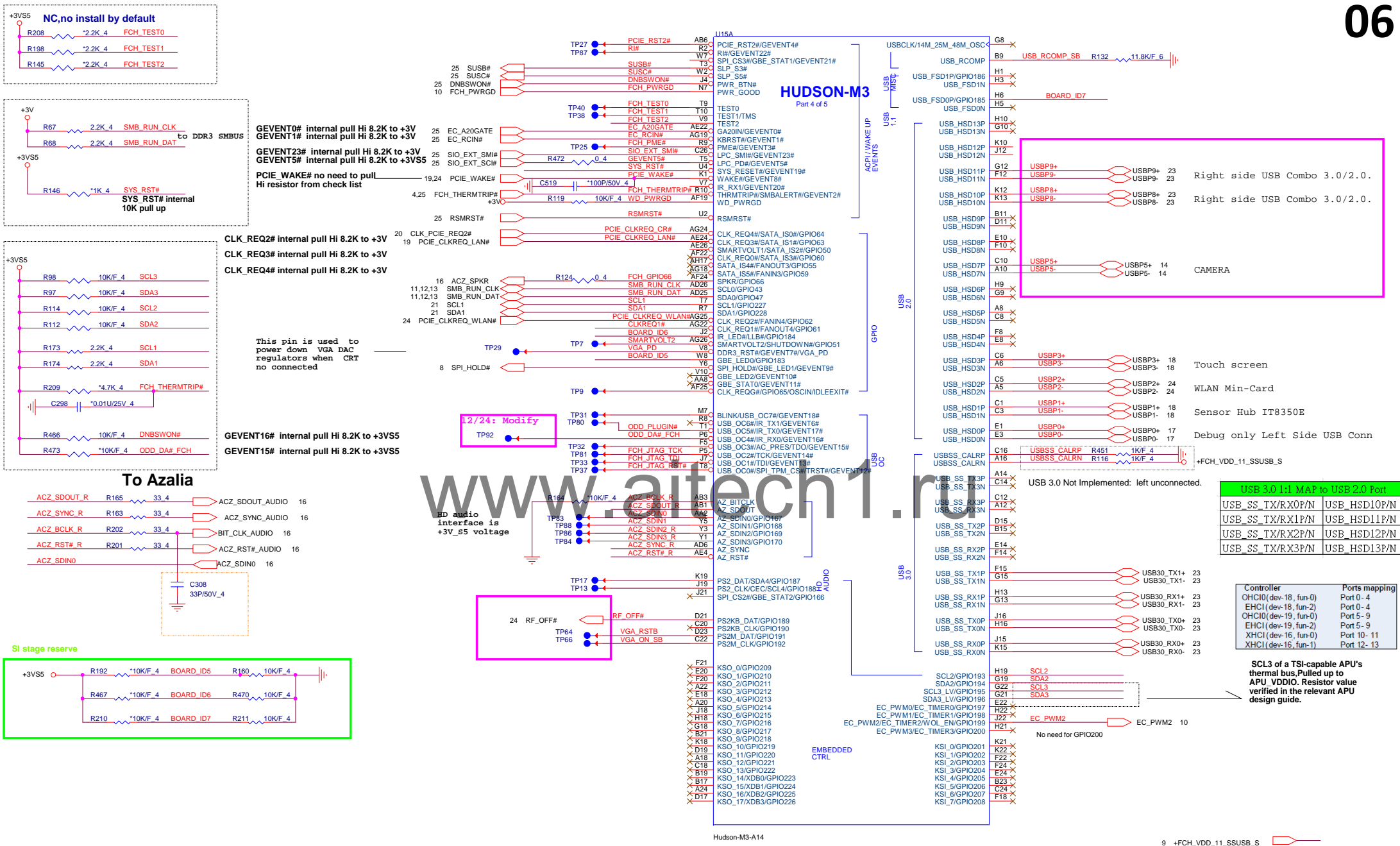
4/19 For Comal,
close to APU.

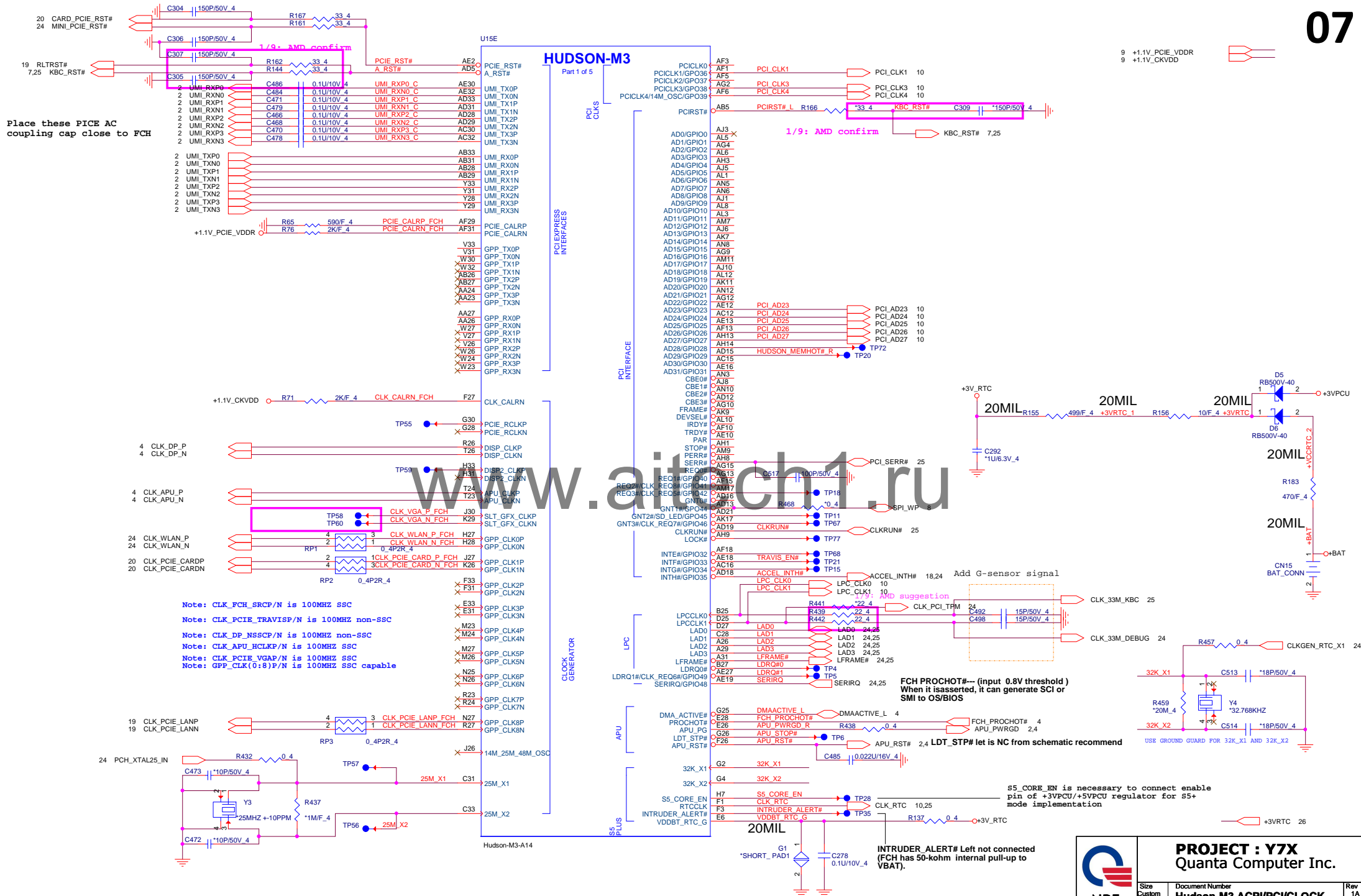
Reserve

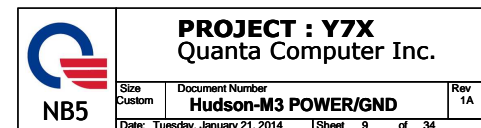
Thermal





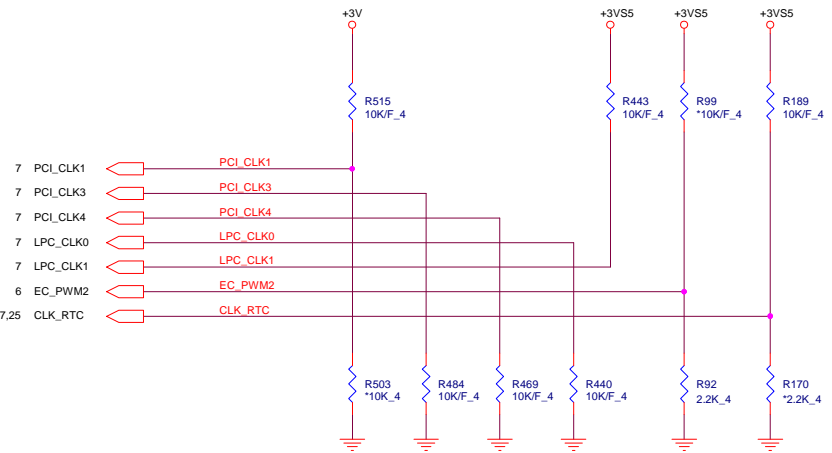






STRAPS PINS

OVERLAP COMMON PADS WHERE POSSIBLE FOR DUAL-OP RESISTORS.

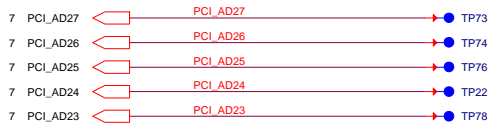


REQUIRED STRAPS

		PCI_CLK1		PCI_CLK3	PCI_CLK4	LPC_CLK0	LPC_CLK1	EC_PWM2	CLK_RTC
PULL HIGH	-----	ALLOW PCIE Gen2 DEFAULT	-----	USE DEBUG STRAP	non Fusion CLOCK MODE	AMD internal EC ENABLED	CLKGEN ENABLED DEFAULT	LPC ROM	S5 PLUS MODE ENABLED DEFAULT
PULL LOW	-----	FORCE PCIE Gen1	-----	IGNORE DEBUG STRAP DEFAULT	FUSION CLOCK MODE DEFAULT	EC DISABLED DEFAULT	CLKGEN DISABLED DEFAULT	SPI ROM	S5 PLUS MODE DISABLED DEFAULT

DEBUG STRAPS

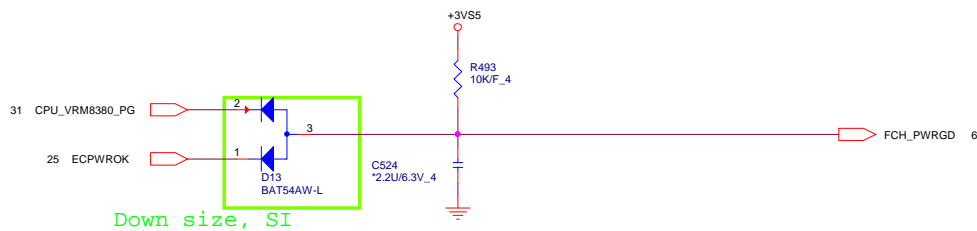
FCH has 15K Internal Pull Up for PCI_AD[27:23]



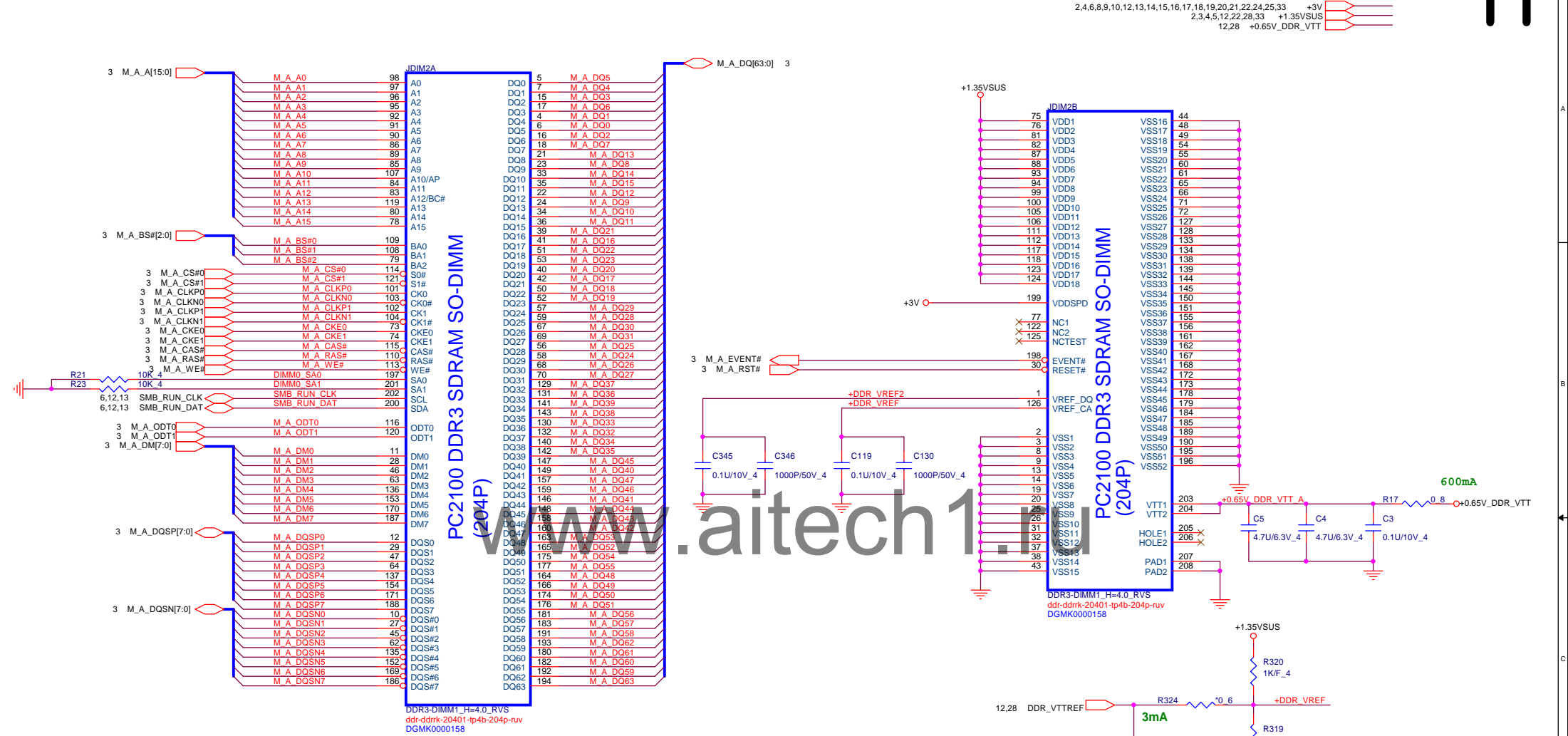
remove reserve pull low resistor
reserve test point only.

	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE PCI PLL DEFAULT	DISABLE ILA AUTORUN DEFAULT	USE FC PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	DISABLE PCI MEM BOOT DEFAULT
PULL LOW	BYPASS PCI PLL	ENABLE ILA AUTORUN	BYPASS FC PLL	USE EEPROM PCIE STRAPS	ENABLE PCI MEM BOOT

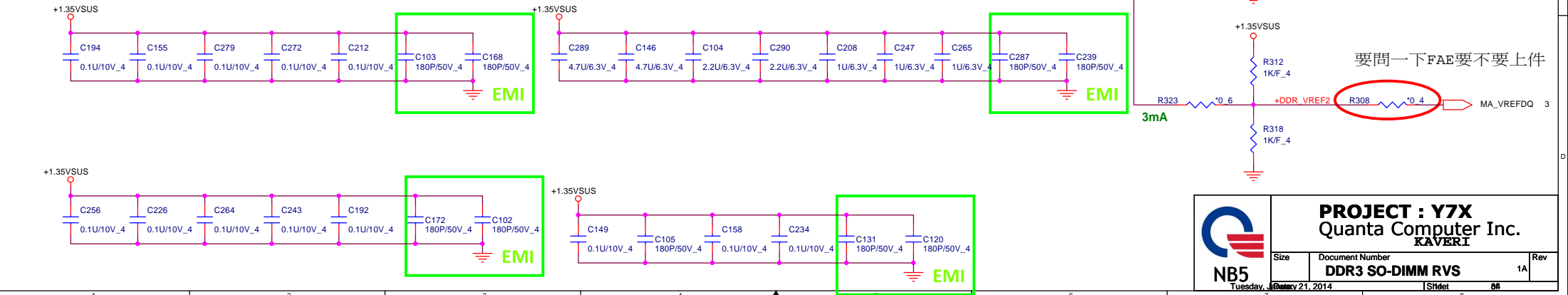
FCH_PWRGD



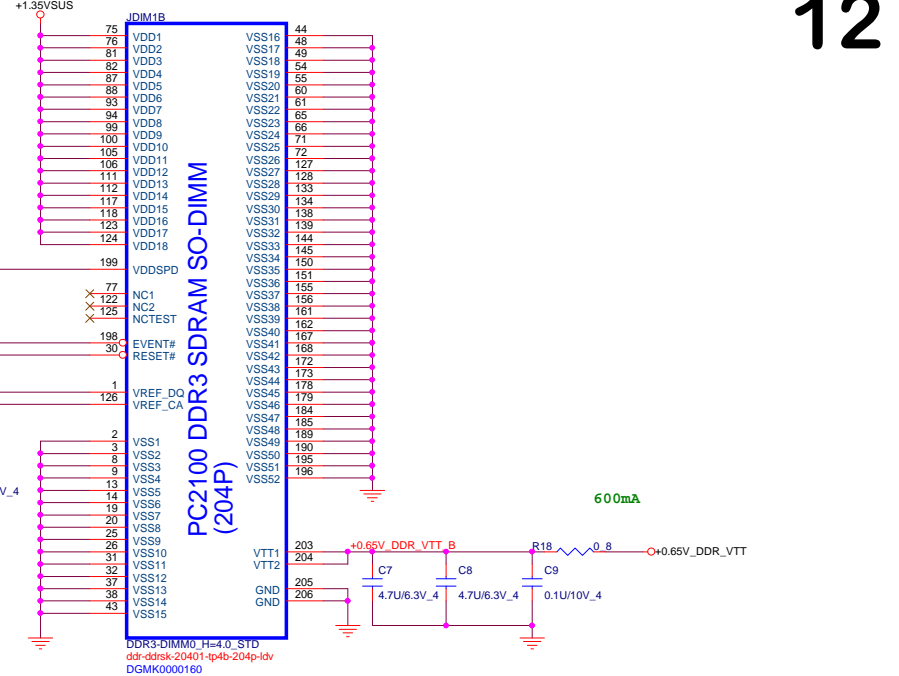
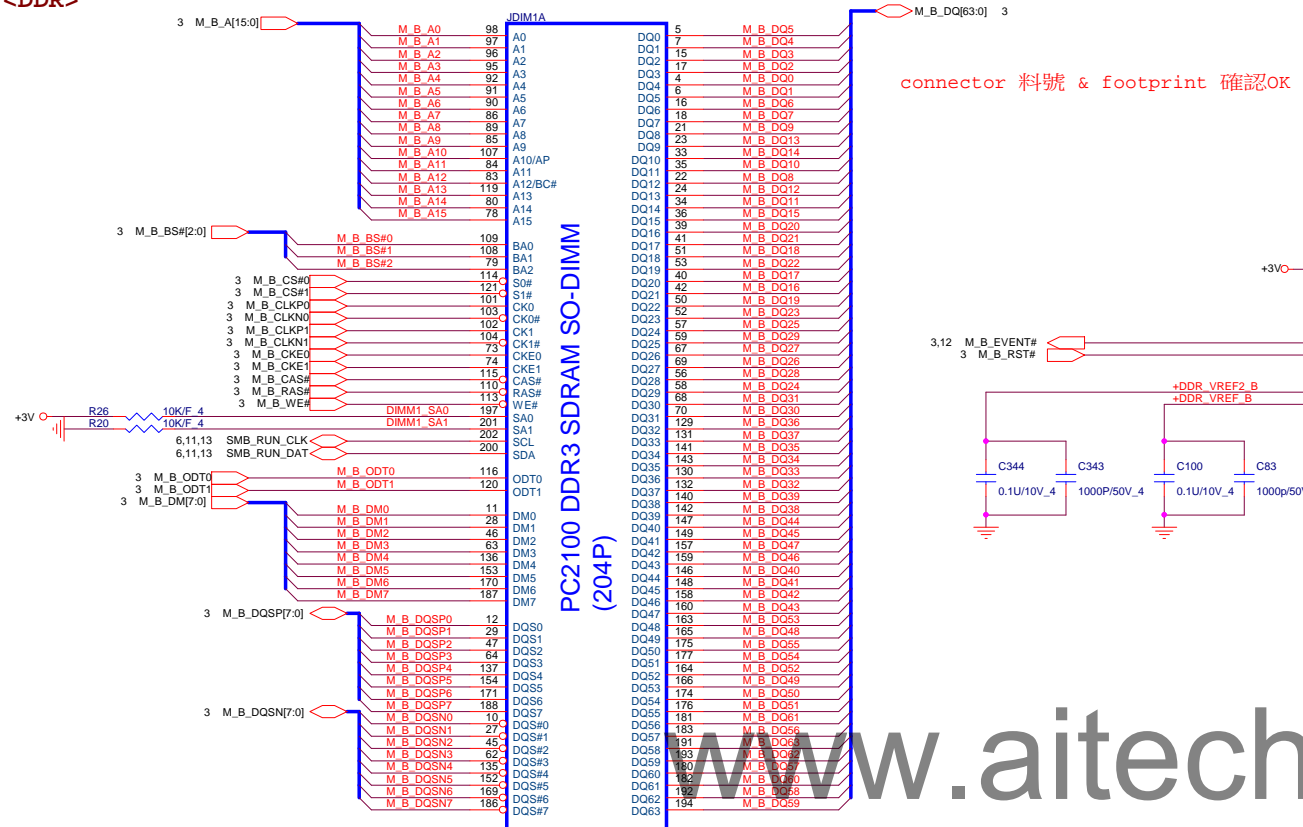
Down size, SI



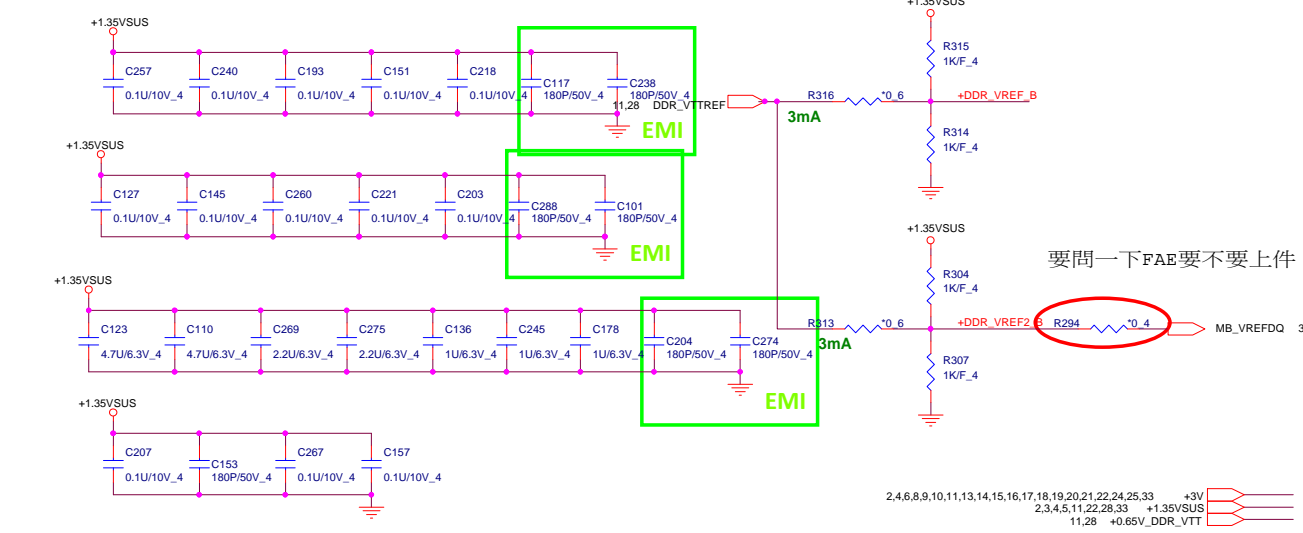
Place these Caps near So-Dimm A



<DDR>

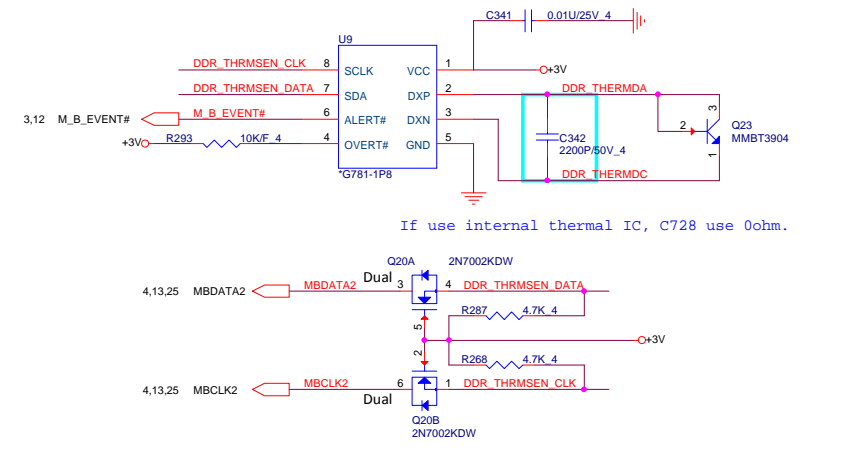


Place these Caps near So-Dimm B



Local Thermal Sensor

DDR3 Thermal Sensor

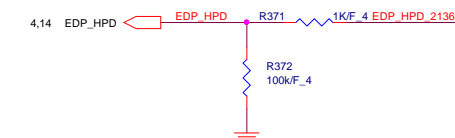
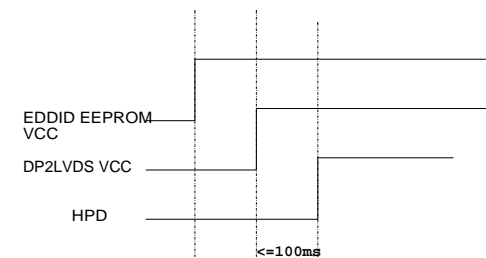


Main:AL000781039	G781-1P8(9Ah)
2nd:AL001412005	EMC1412-2-ACZL-TR(9Ah)
Main:AL001412003	EMC1412-1-ACZL-TR(98h)
2nd:AL000431014	TMP431ADGKR(98h)

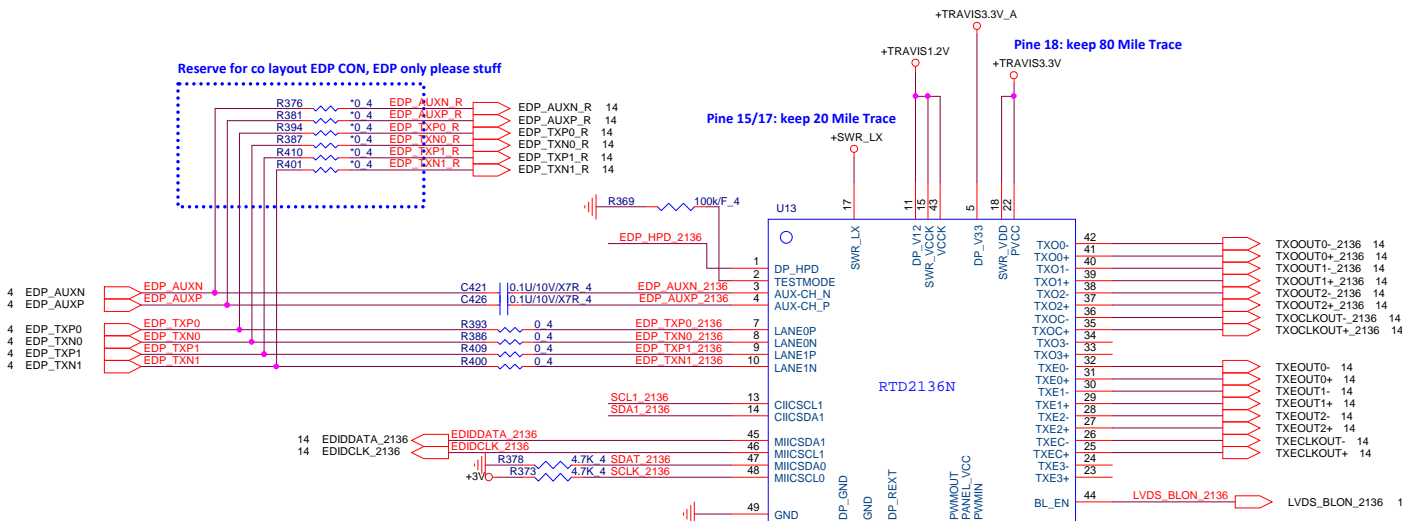
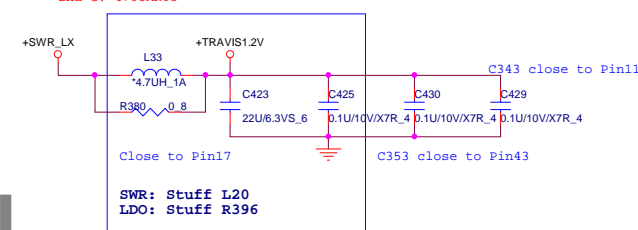
PROJECT : Y7X
Quanta Computer Inc.
KAVERI

Size	Document Number	Rev
	DDR3 SO-DIMM STD	1A

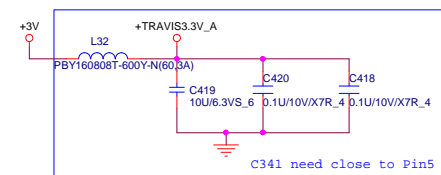
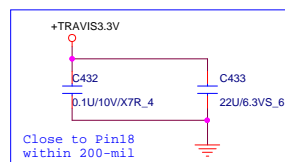
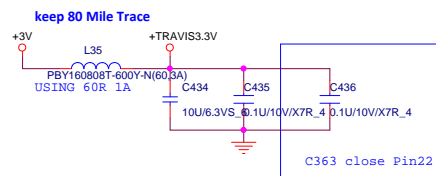
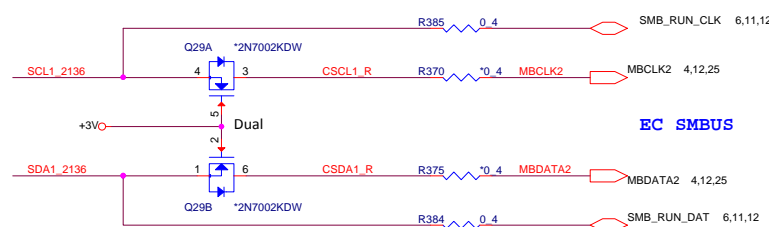
Tuesday, January 21, 2014 15:08 of 34



L20: need use CV-4709MN00 for Vendor suggestion
2nd CV-4708MN03

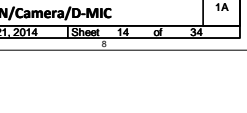
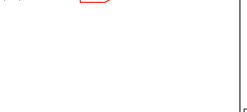
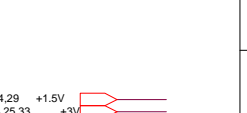
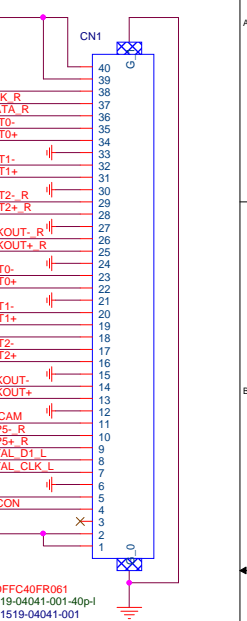
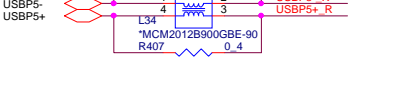
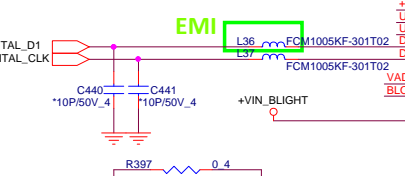
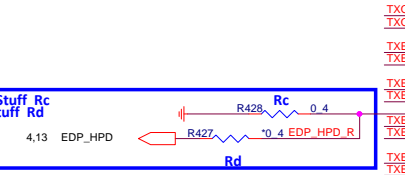
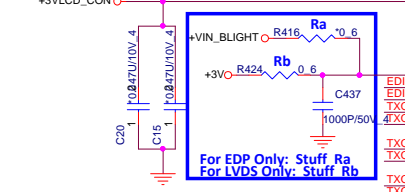
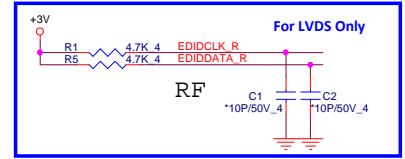
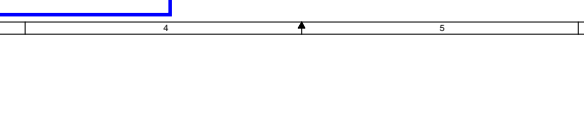
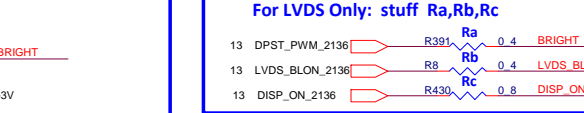
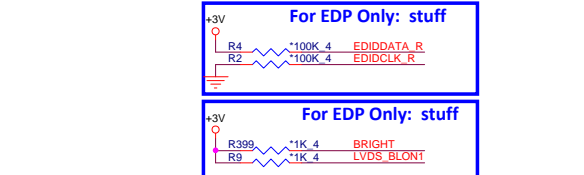
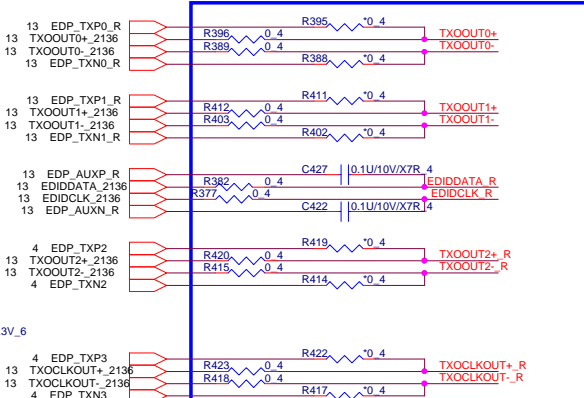
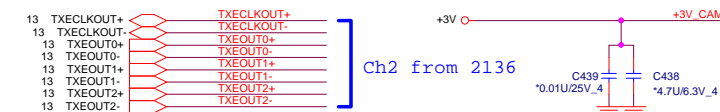
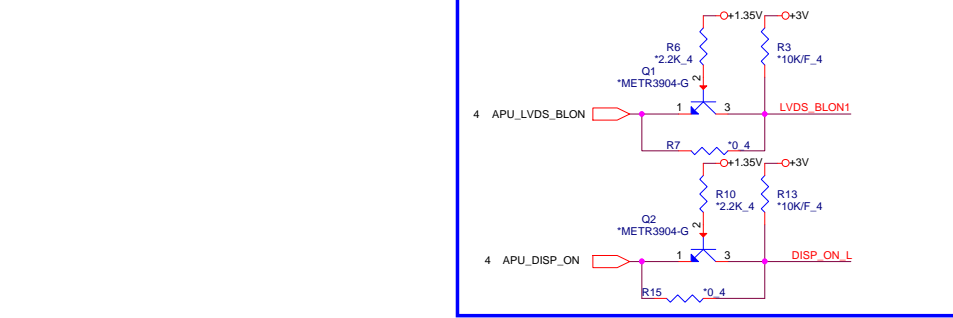
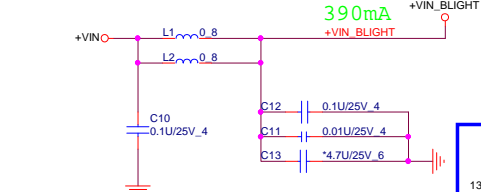
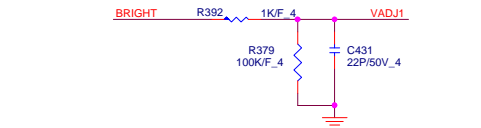
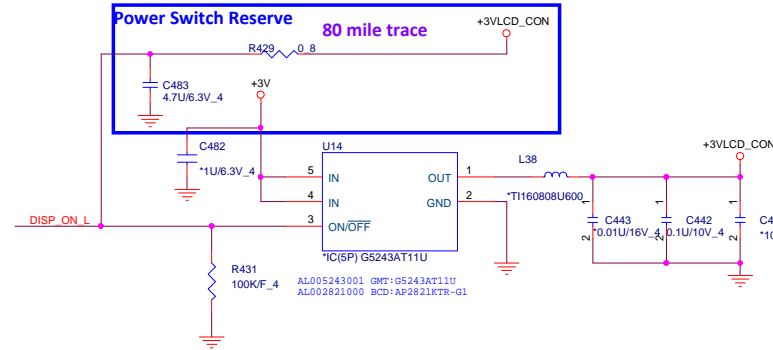
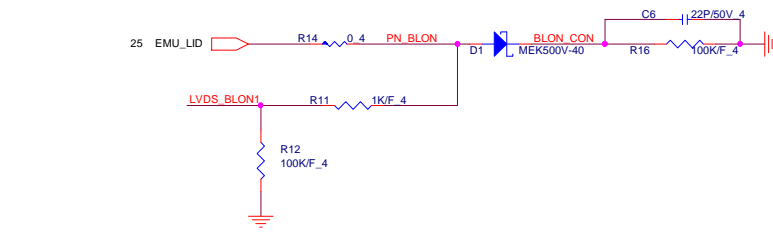


		MODE_CFG0(PIN47)	
		0	1
MODE_CFG1(PIN48)	0	x	EP MODE
	1	ROM ONLY MODE	EEPROM MODE



LVDS conn.

14



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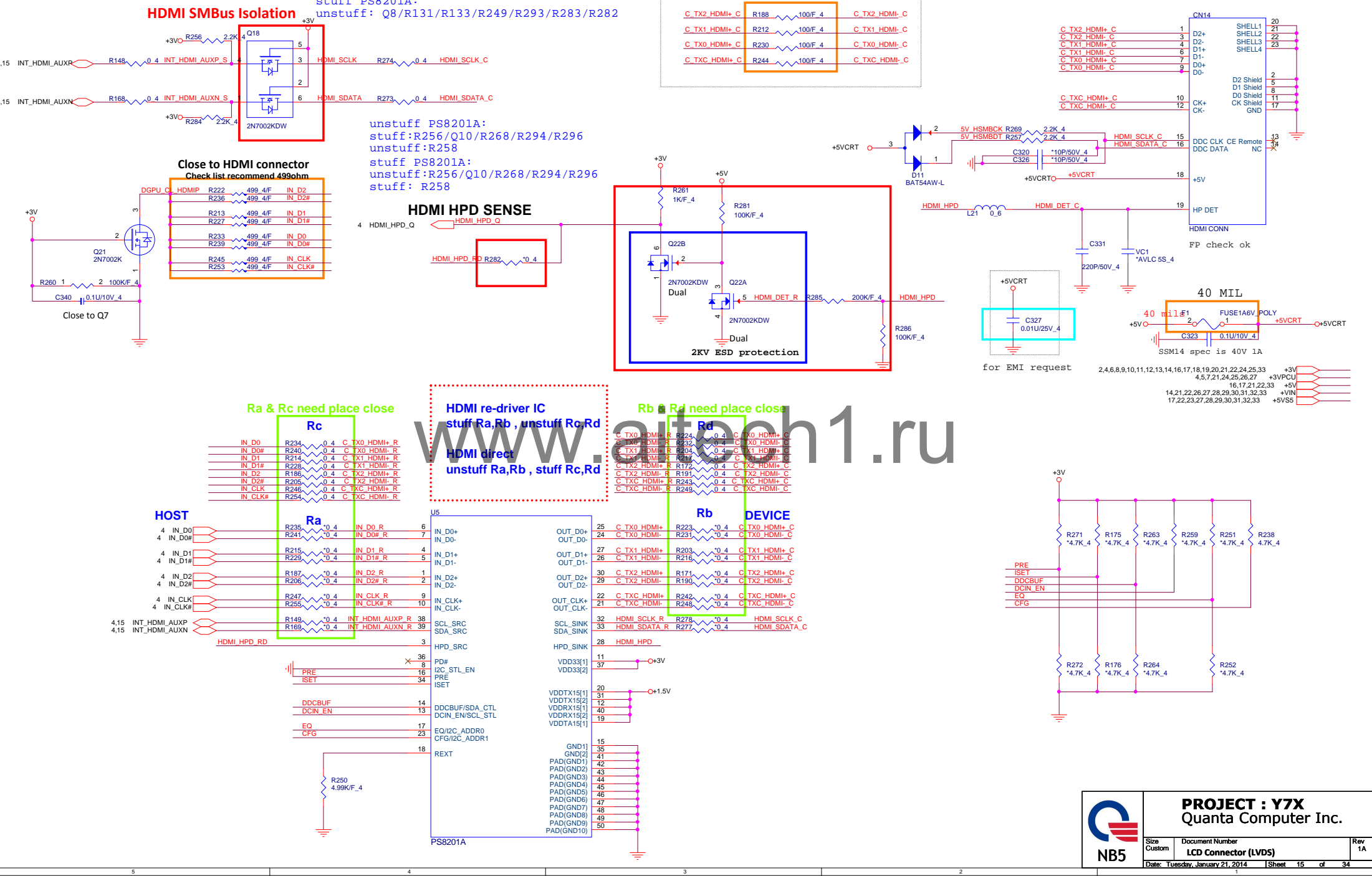
For EDP Only: stuff
R37/R45/R72/R73/C30120/C30121/R50368/R50370/R50372/R50374
For LVDS only: stuff
R17/R18/R11/R120/R33/R34/R50367/R50369/R50372/R50374

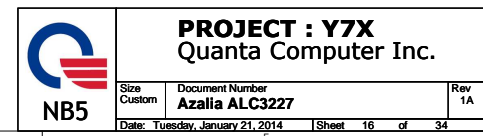
For EDP Only: stuff
R4
R2

For EDP Only: stuff
R399
R9

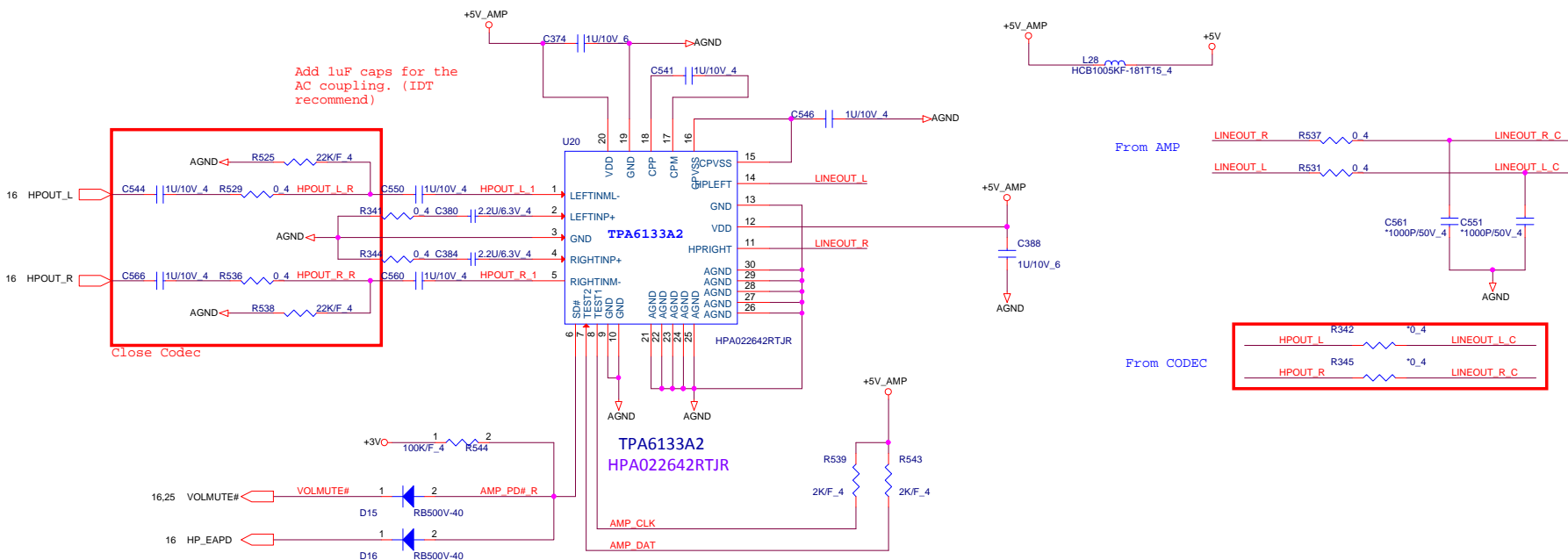
For LVDS Only: stuff Ra,Rb,Rc
R391
R8
R430

NB5	PROJECT : Y7X Quanta Computer Inc.		
	Size Custom	Document Number LCD CONN/Camera/D-MIC	Rev 1A
Date: Tuesday, January 21, 2014	Sheet 14	of 34	

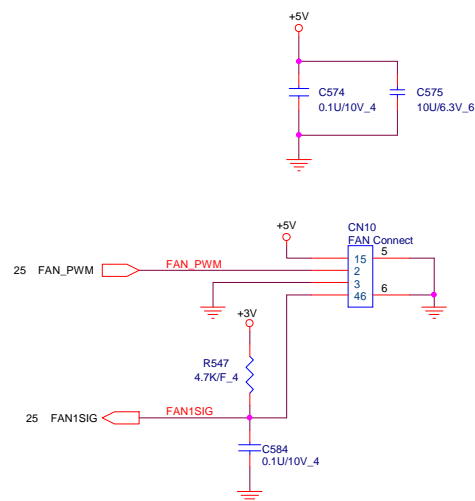




Add 1uF caps for the
AC coupling. (IDT
recommend)

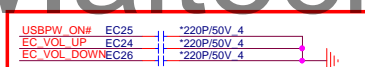


CPU FAN

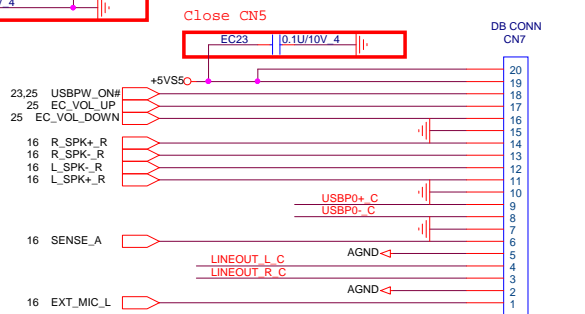


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Close CN5

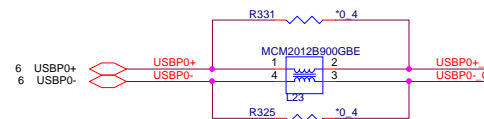


need check PN& FP

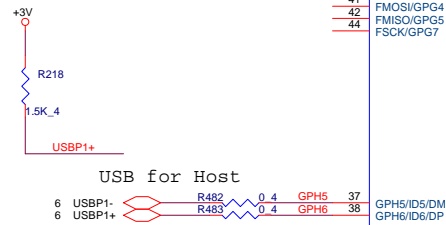
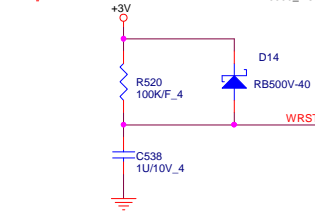
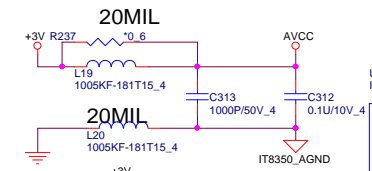
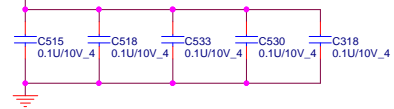


SPK
USB2.0

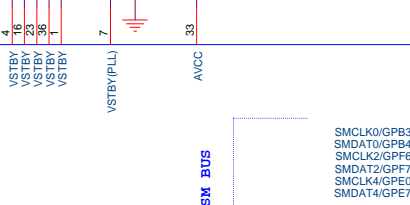
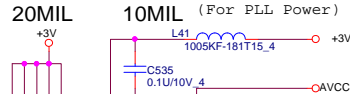
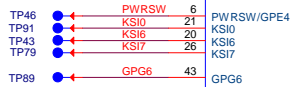
AUDIO COMBO JACK



Place all capacitors close to IT8350.



Please do not place any pull-up resistor on GPG6 (Reserved hardware strapping).



**IT8350E
LQFP-48**

USB

GPIO

PWM

UART

A/D

SSPI

CLOCK

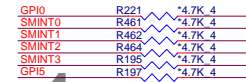
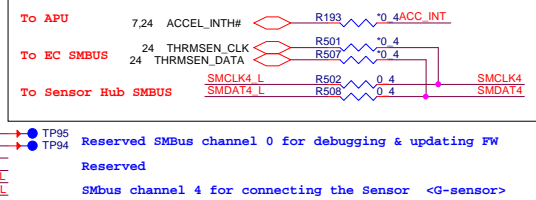
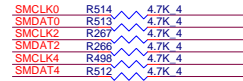
Reserved TX/RX for debugging

if no use ADC function,
please pullDown to GND

GPG2 can't floating

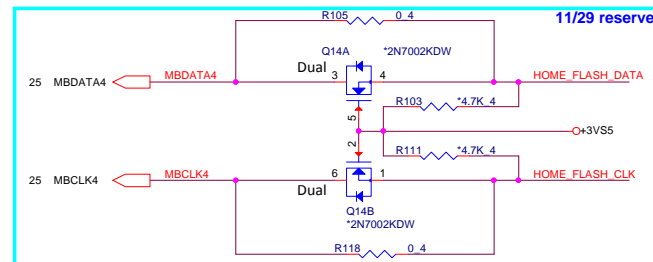
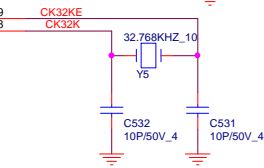
GPG2 Pull High Enable mirror function.
GPG2 Pull Low Disable mirror function.

External crystal is must be item
when USB func. is used !

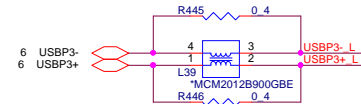
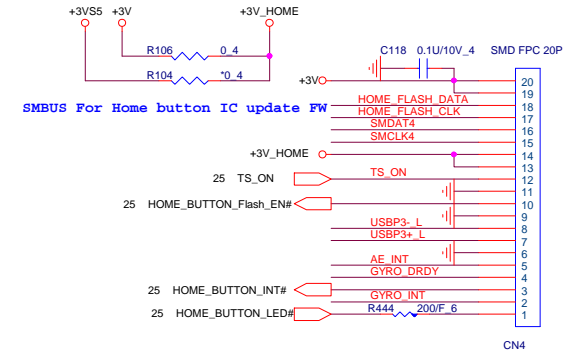
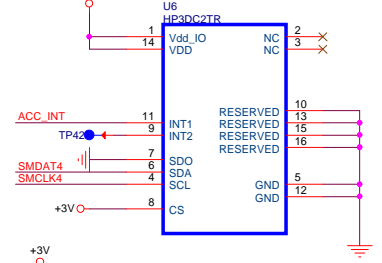


GPG2 can't floating

GPG2 Pull High Enable mirror function.
GPG2 Pull Low Disable mirror function.

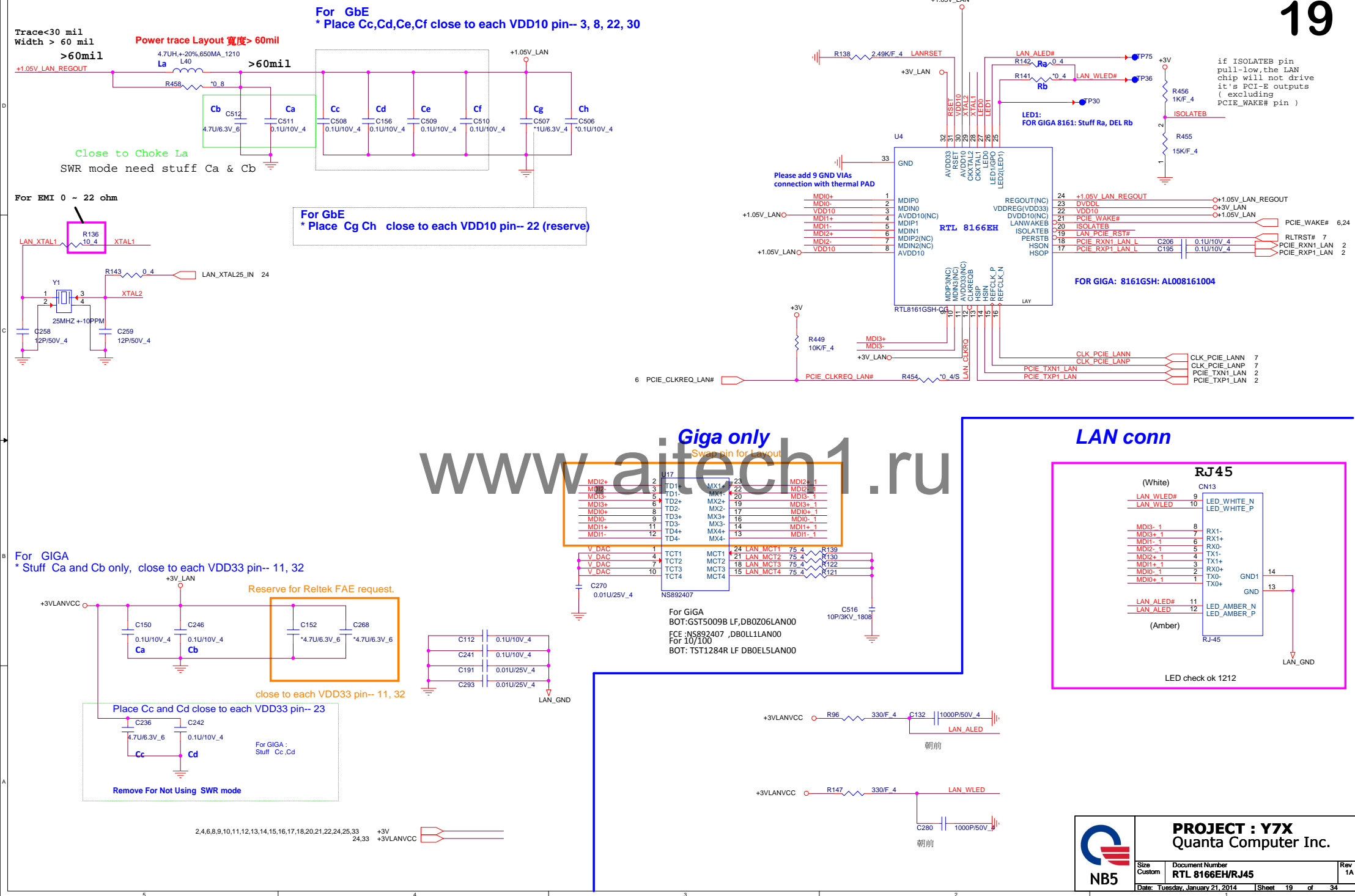


Accelerometer Sensor
Put it on MB side



PROJECT : Y7X
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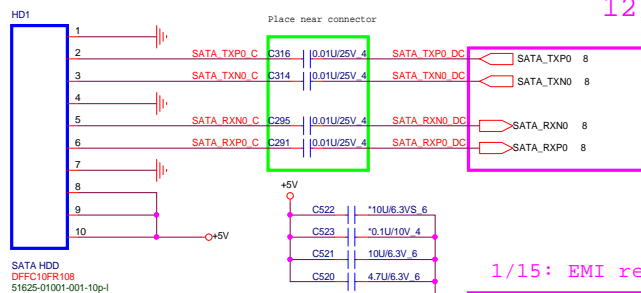
Size	Document Number	Rev
Custom	Sensor Hub 8350/HP3DC2/TS	1A
Date: Tuesday, January 21, 2014	Sheet 18 of 34	





SATA HDD CONNECTOR FFC TYPE

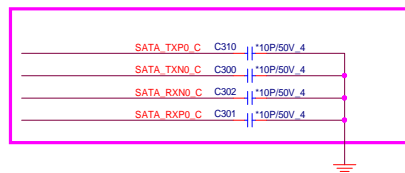
22



12/25: Del SATA re-driver

1/15: EMI reserve

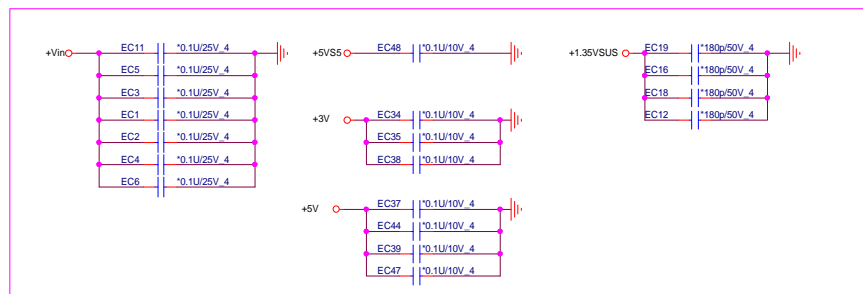
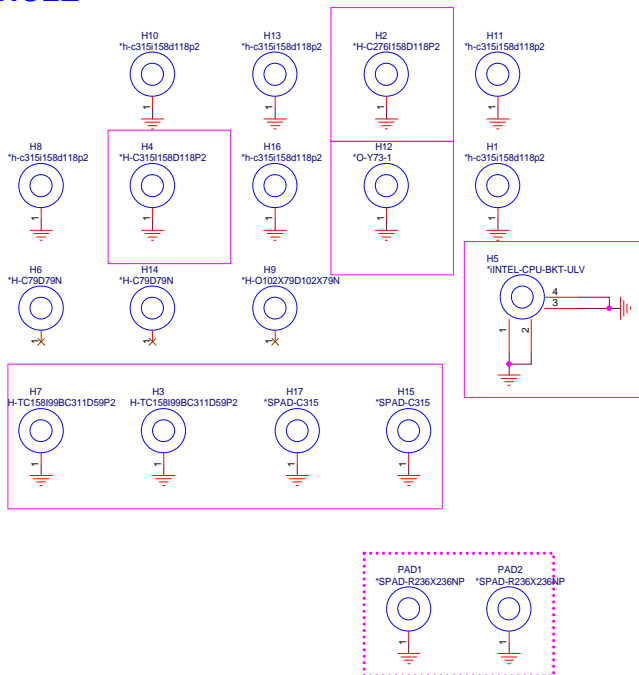
Bypass CAP close conn



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HOLE

EMI reserve



PROJECT : Y7X
Quanta Computer Inc.

2,4,6,8,9,10,11,12,13,14,15,16,17,18,19,20,21,24,25,33
15,16,17,21,33

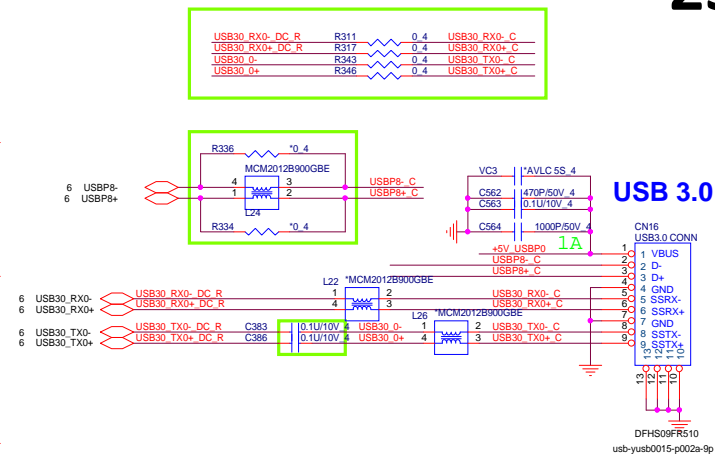
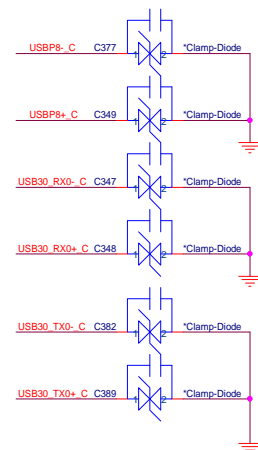
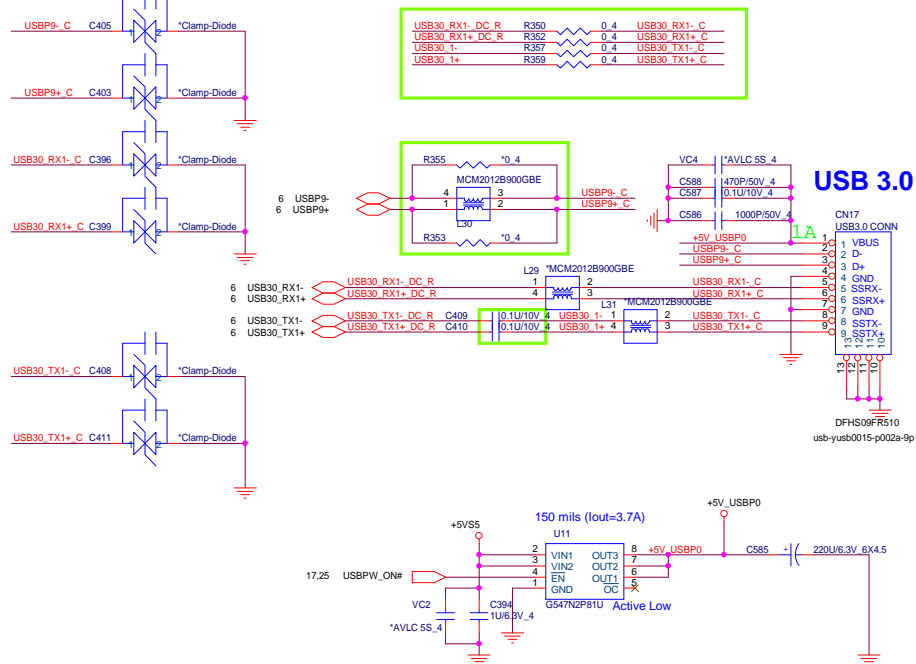
+3V
+5V



Size Custom	Document Number HDD/re-driver/HOLE	Rev 1A
Date: Tuesday, January 21, 2014	Sheet 22 of 34	

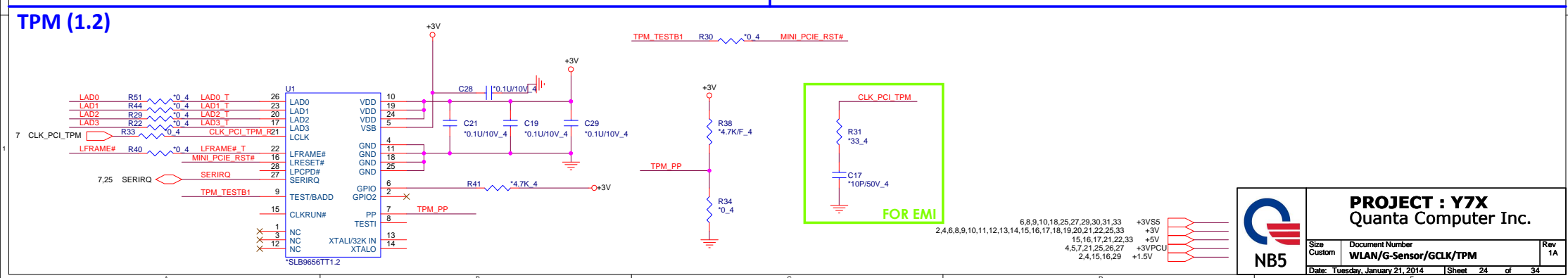
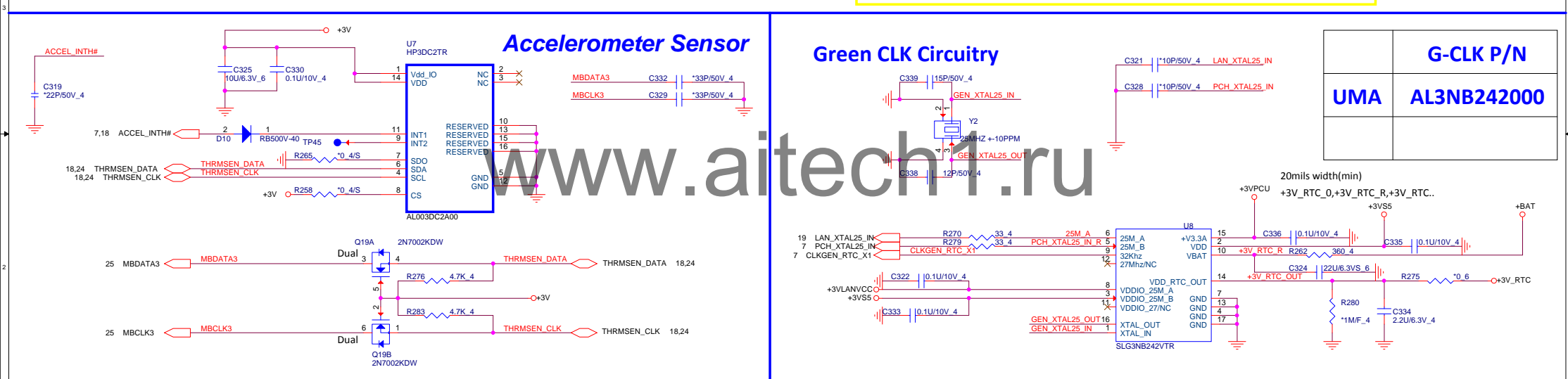
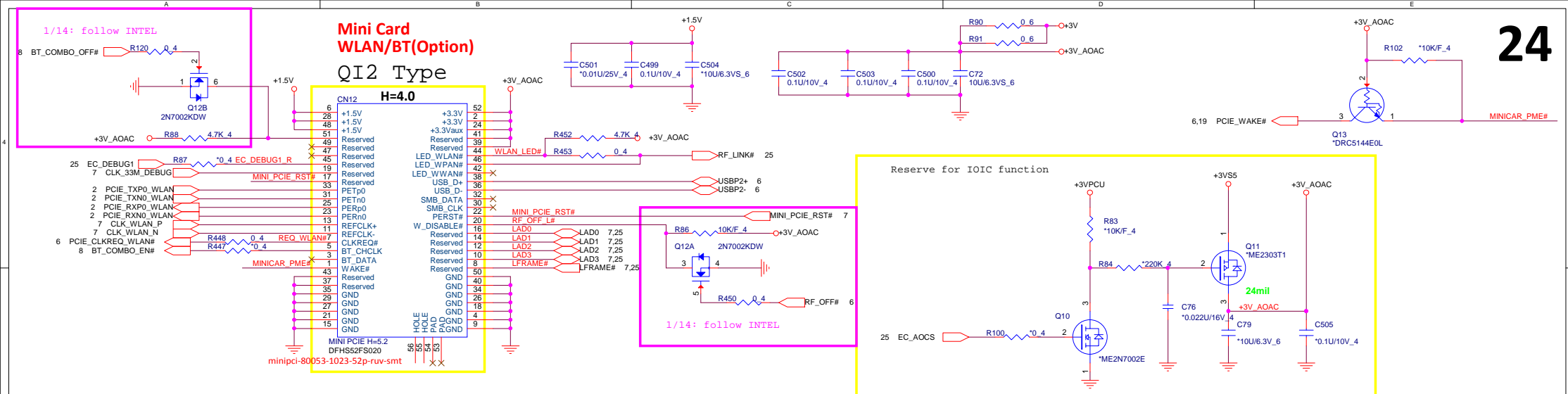
USB 2.0/3.0 Combo

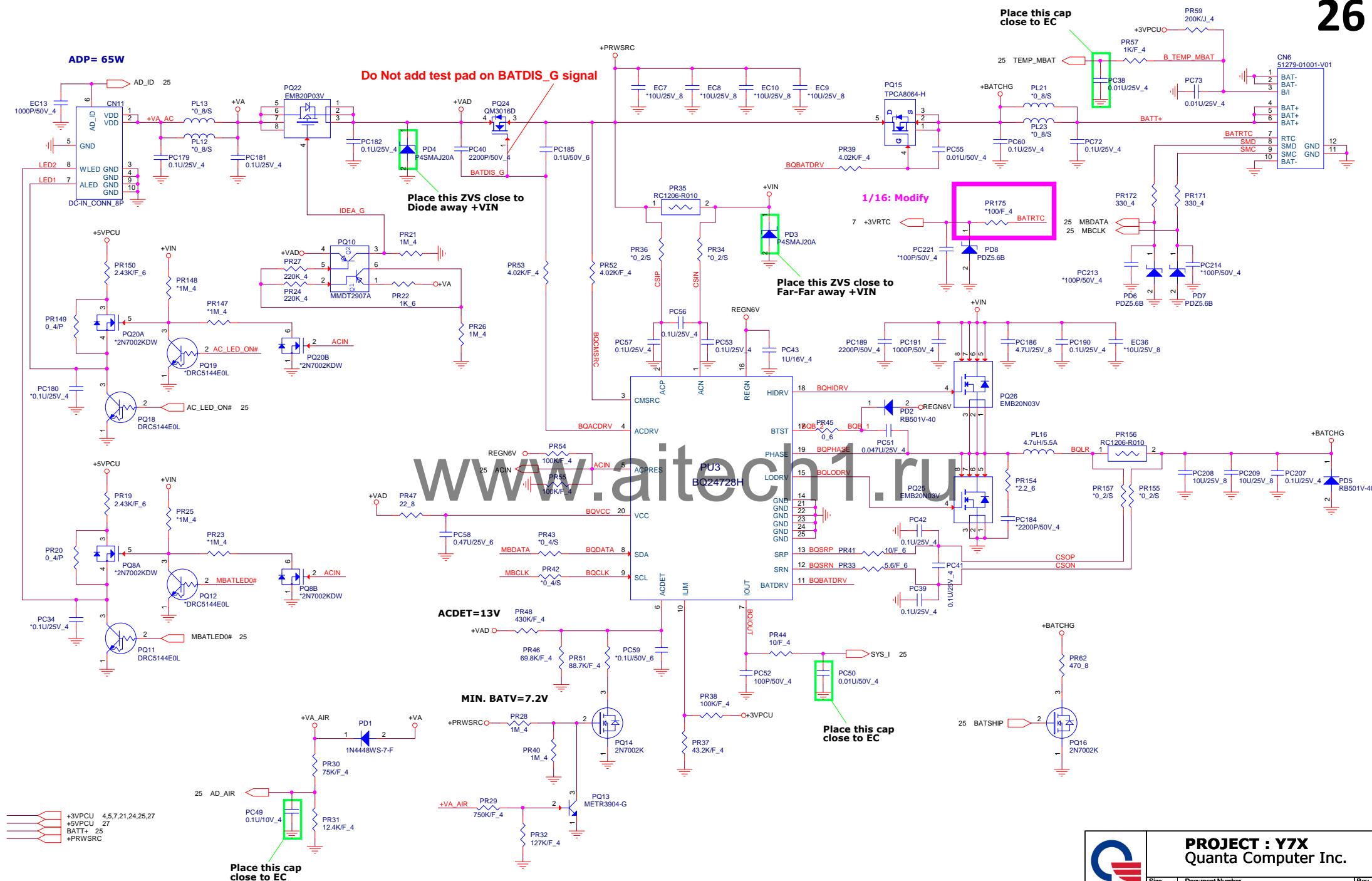
23

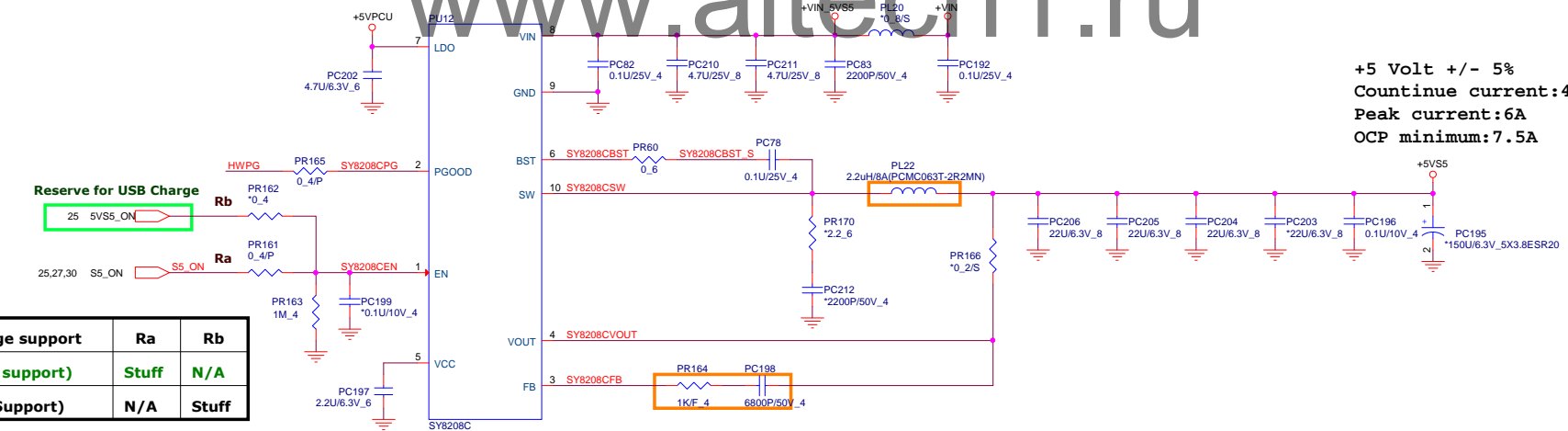
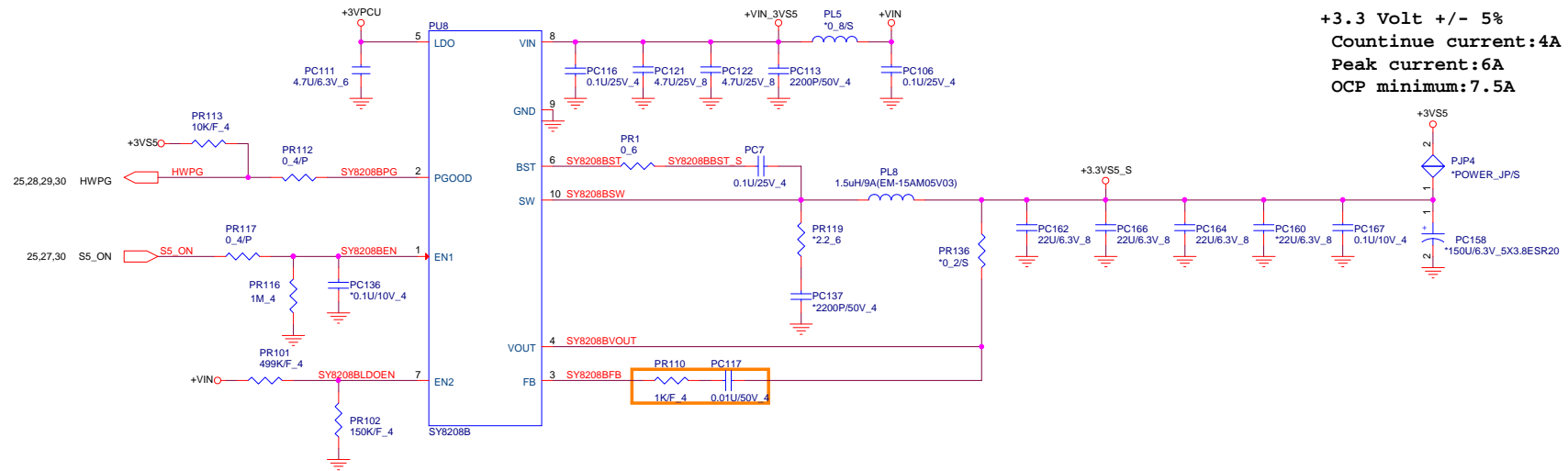


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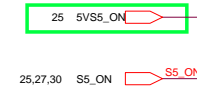
12/25: Del USB re-driver



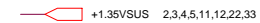


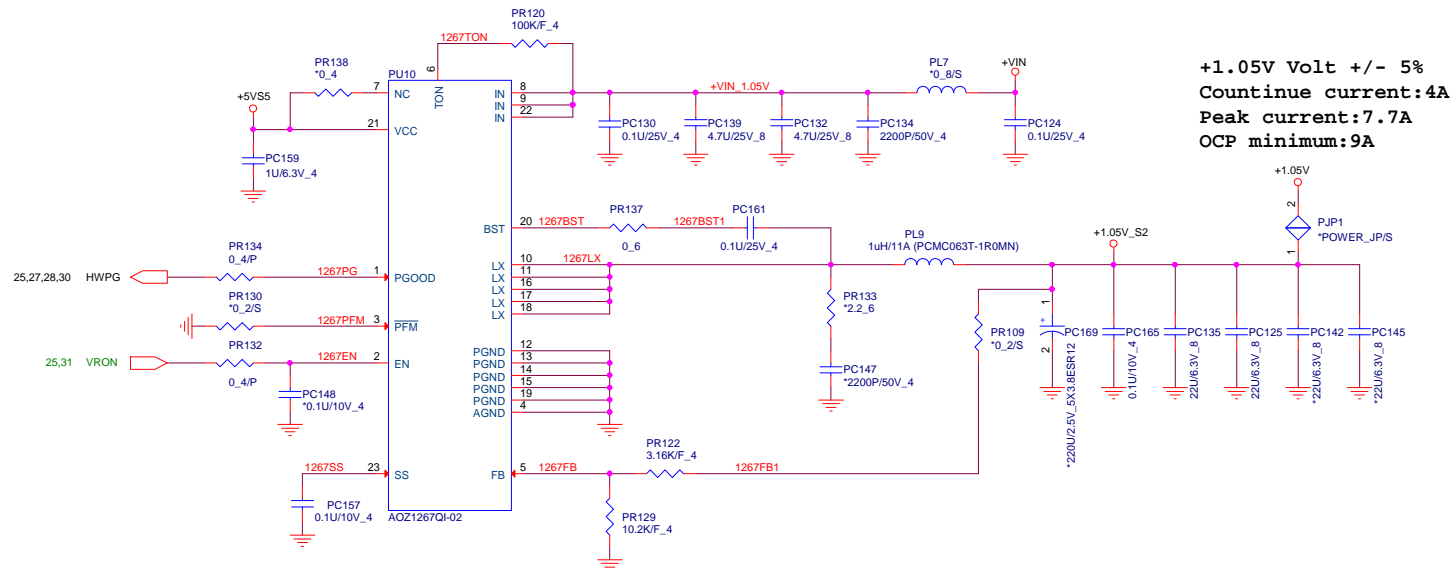


Reserve for USB Charge

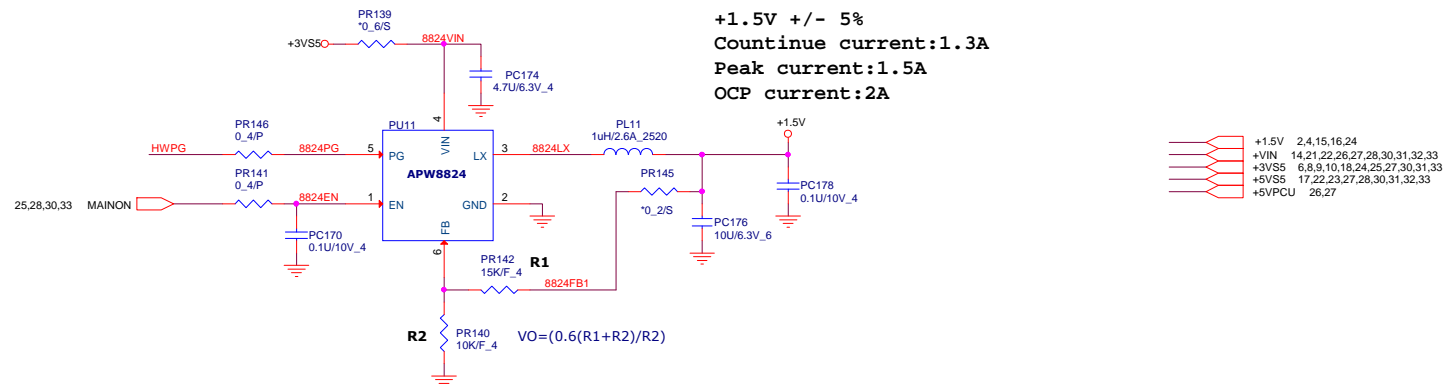


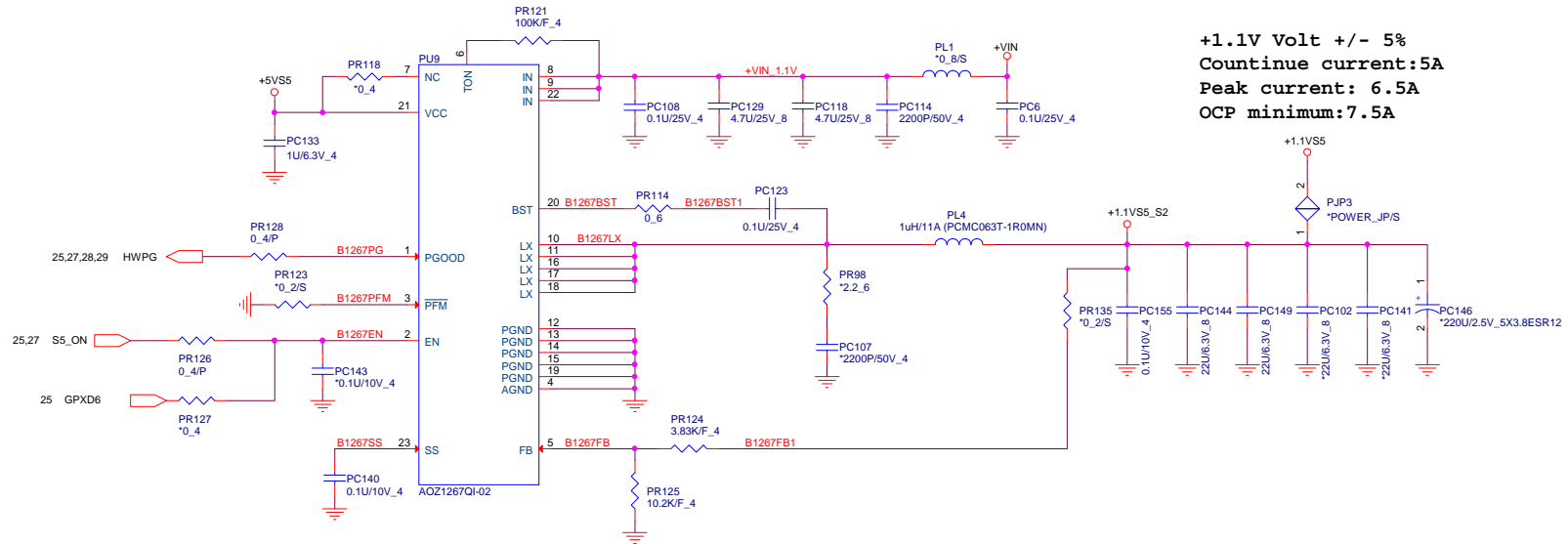
USB charge support	Ra	Rb
Napa (No support)	Stuff	N/A
Whisky (Support)	N/A	Stuff





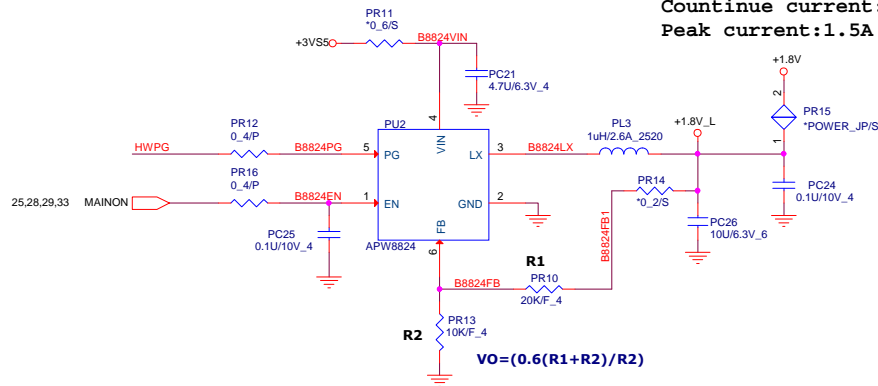
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+1.8V +/- 5%
Countinue current:0.8A
Peak current:1.5A

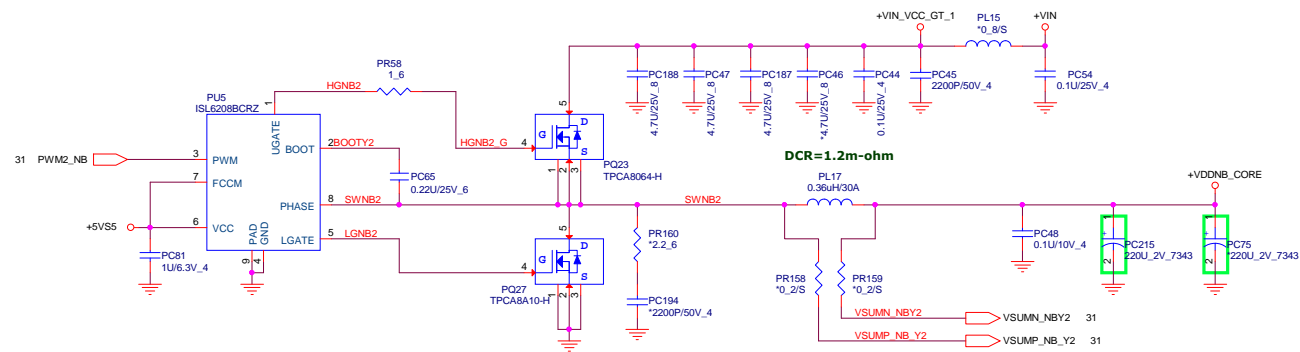
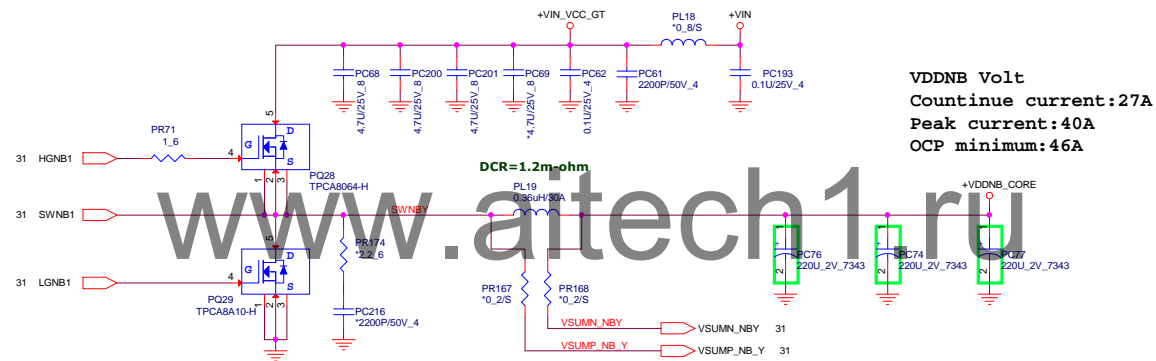
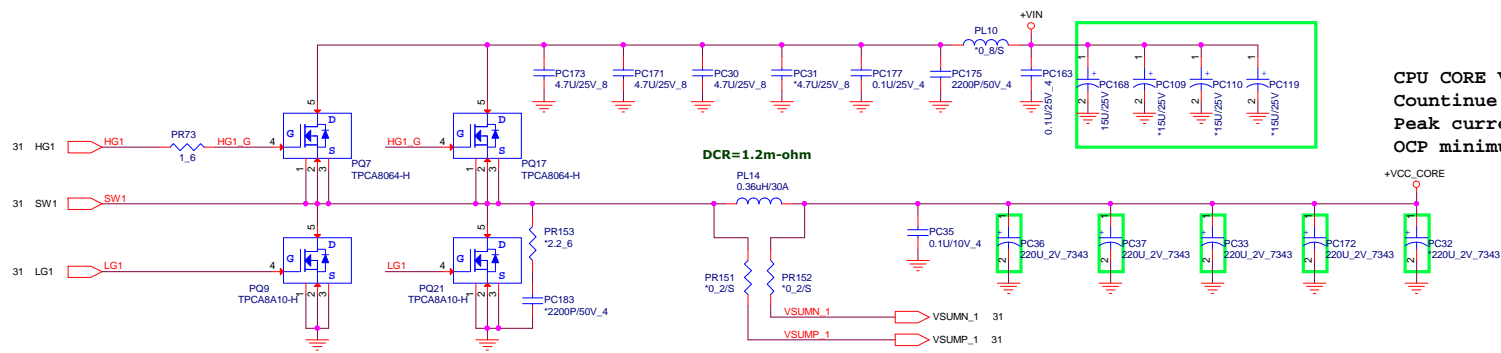


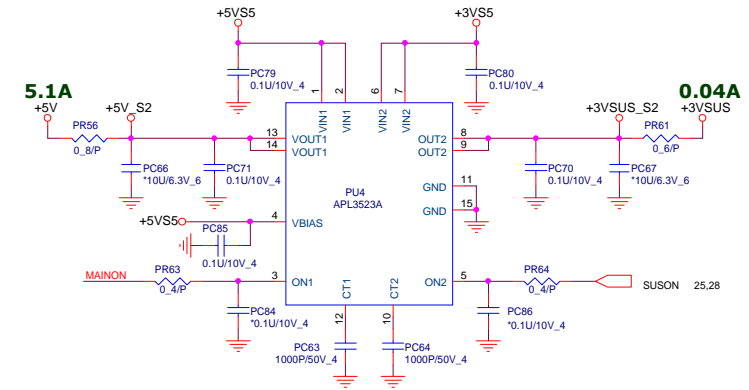
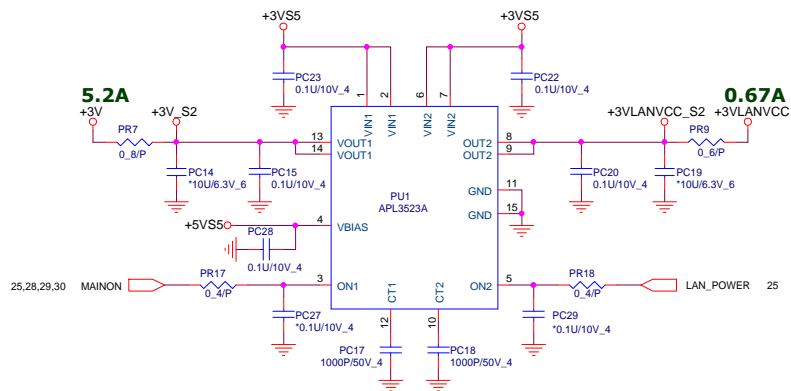
+VIN	14,21,22,26,27,28,29,31,32,33
+3VS5	6,8,9,10,18,24,25,27,29,31,33
+5VS5	17,22,23,27,28,29,31,32,33
+1.1VS5	9,33
+5VPCU	26,27



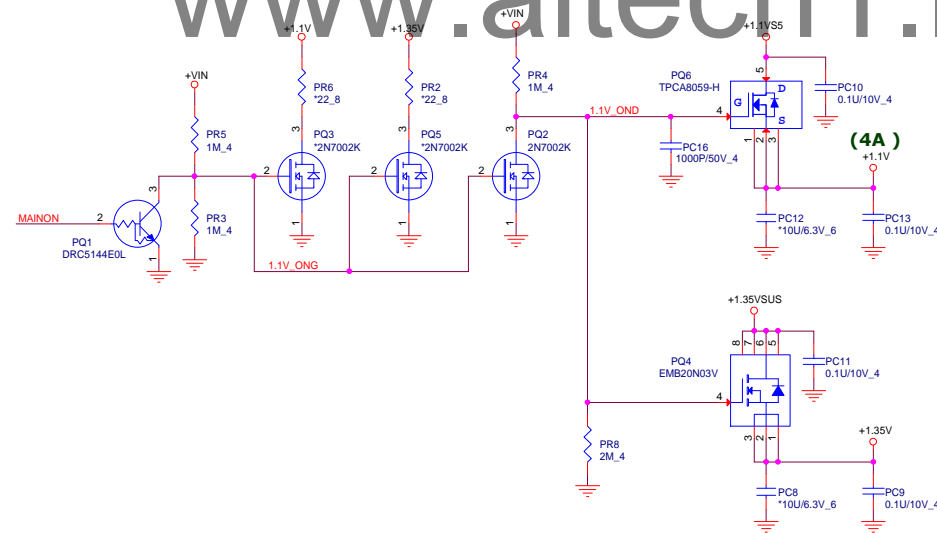
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2,4,6,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,24,25	+3V
15,16,17,21,22	+5V
14,21,22,26,27,28,29,30,31,32	+VIN
6,8,9,10,18,24,25,27,29,30,31	+3VS5
17,22,23,27,28,29,30,31,32	+5VS5
19,24	+3VLAVCC



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